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HSF Property:ROHS or Halogen-Free(5L3?)

GL10FG

2011.08..31

21-OCT-2009		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER					TITLE MODEL PROJECT FUNCTION			
DESIGN					Everest Main Board			
CHECK								
RESPONSIBLE								
SIZE				VER	SIZE	CODE	DOC NUMBER	REV
FILE NAME					C	CS	13100000X-00	X01
PIN		XXX			SHEET		1	of 25

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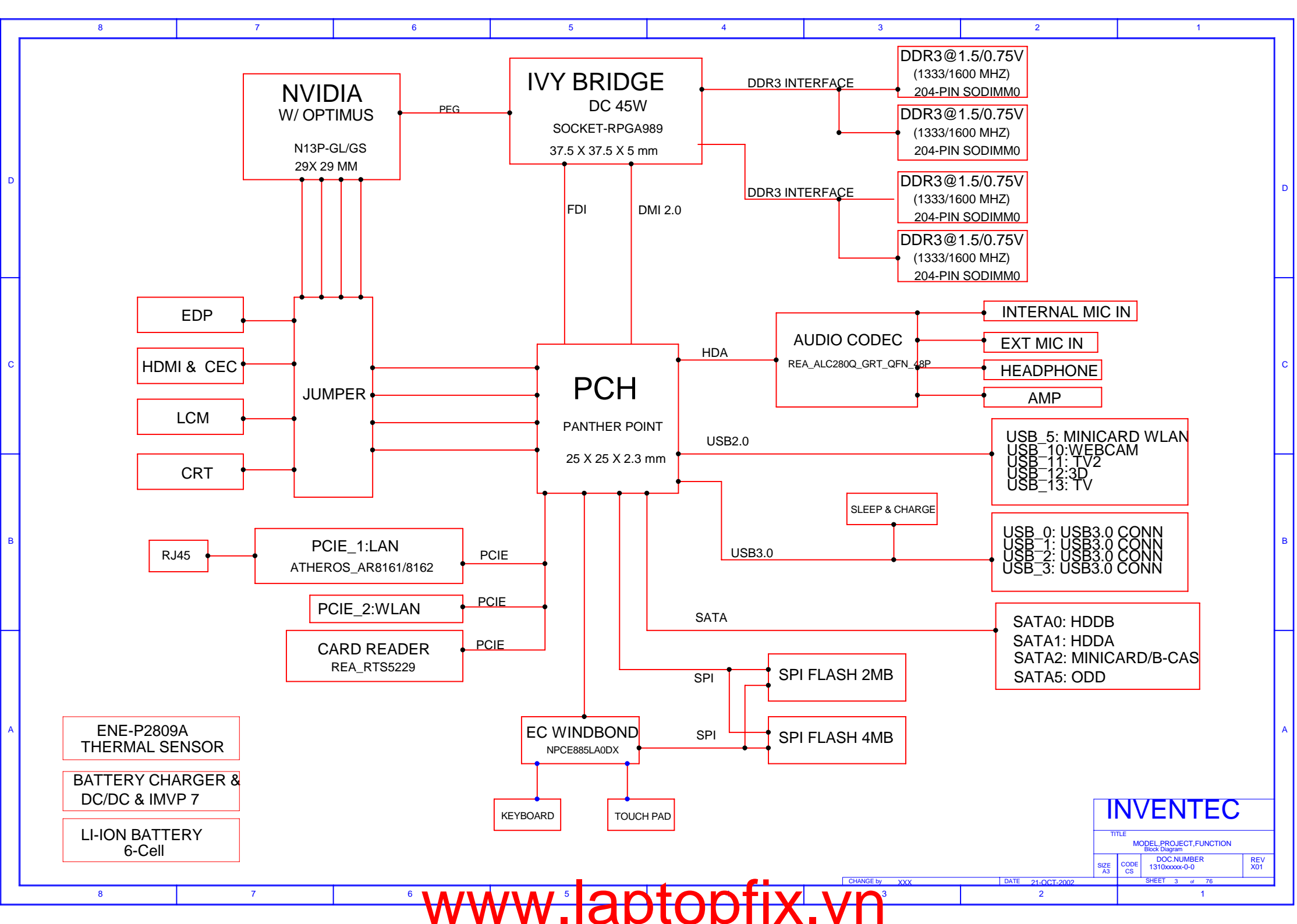
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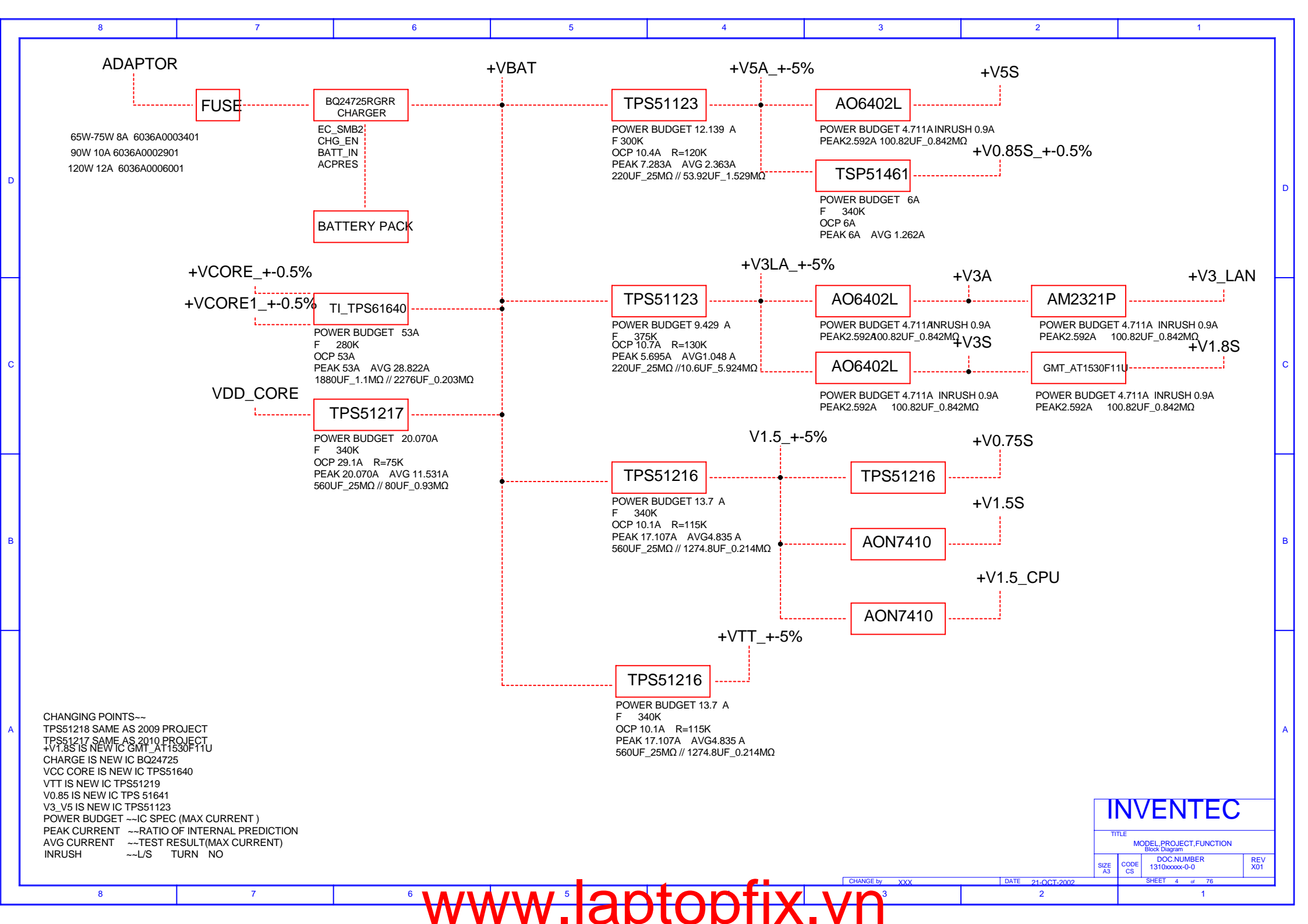
**INVENTEC**

TITLE  
MODEL,PROJECT,FUNCTION  
Block Diagram

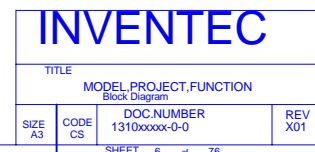
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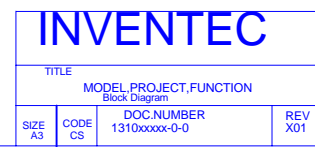




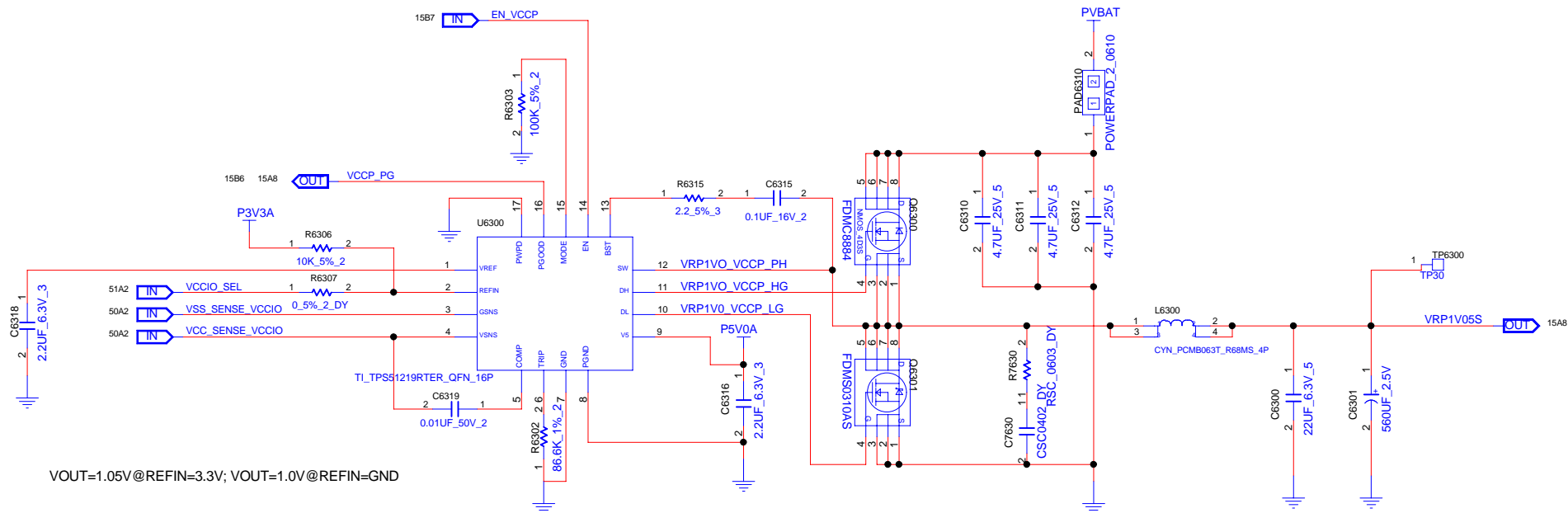










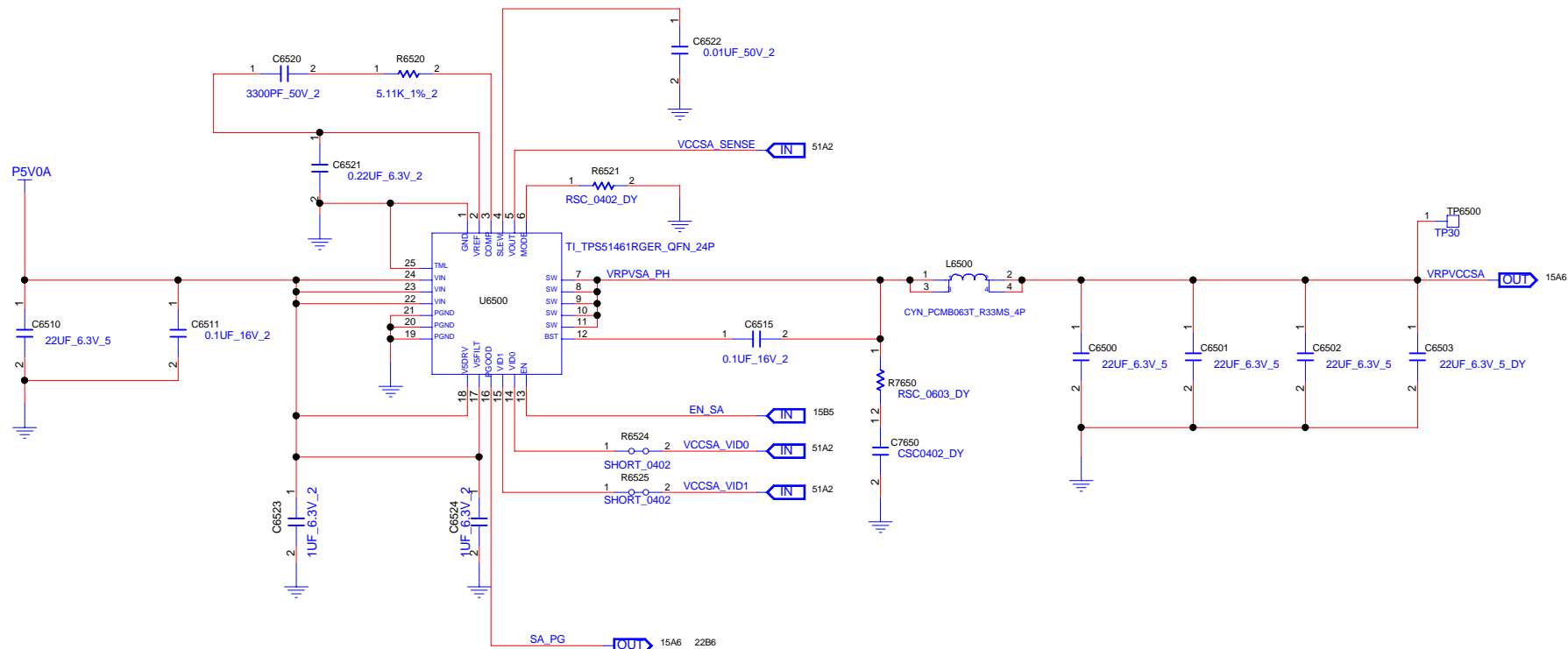


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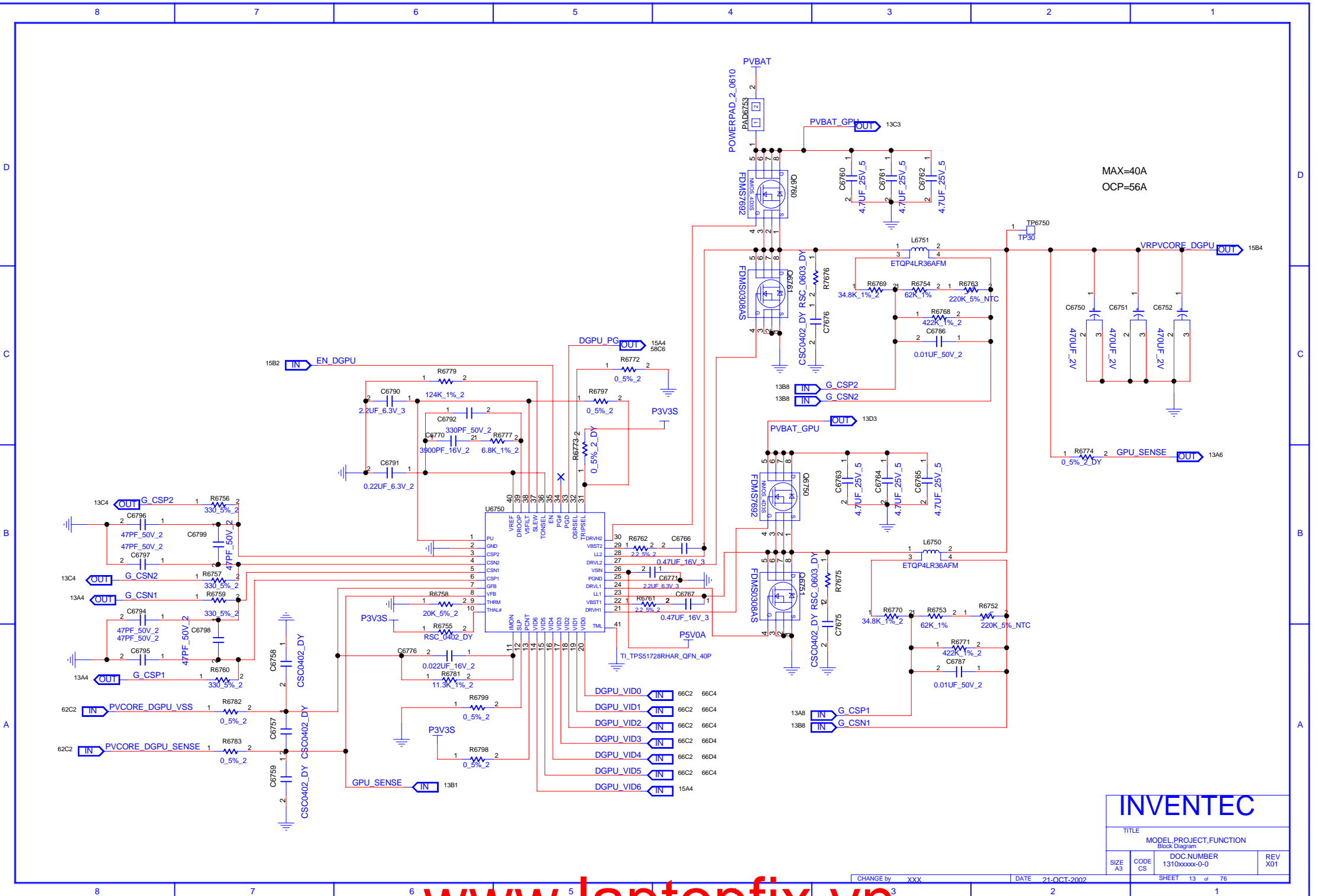
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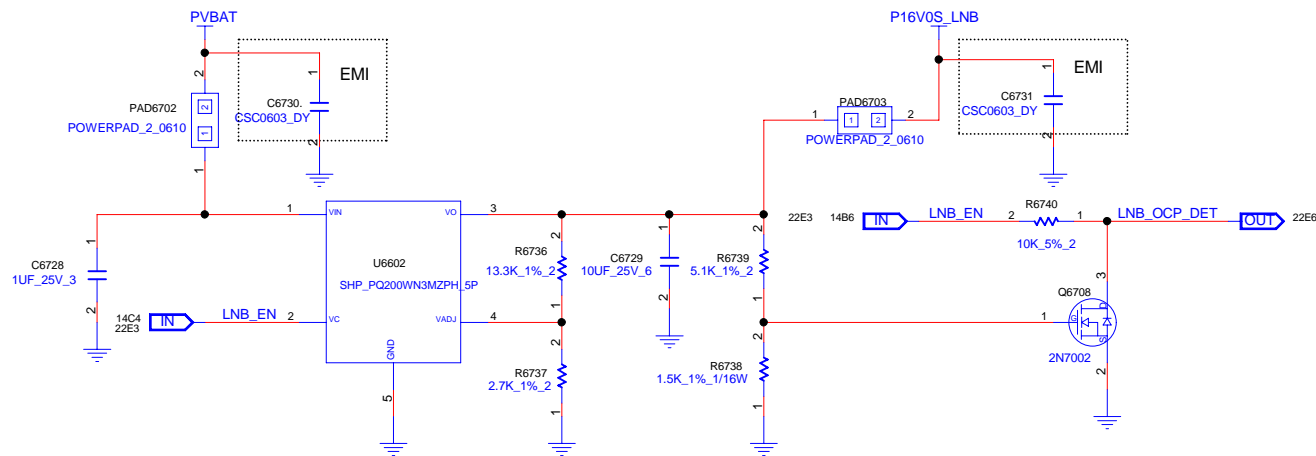
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# LNB POWER



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TITLE MODEL PROJECT FUNCTION

Block Diagram

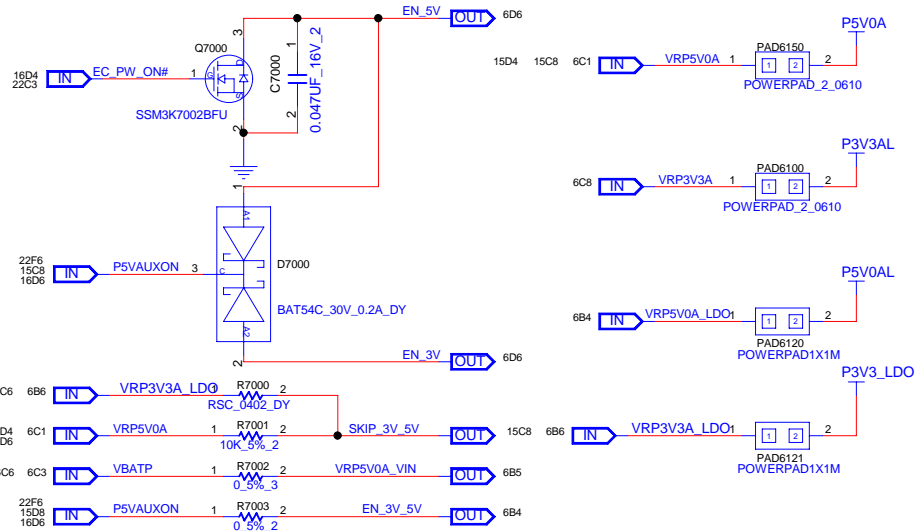
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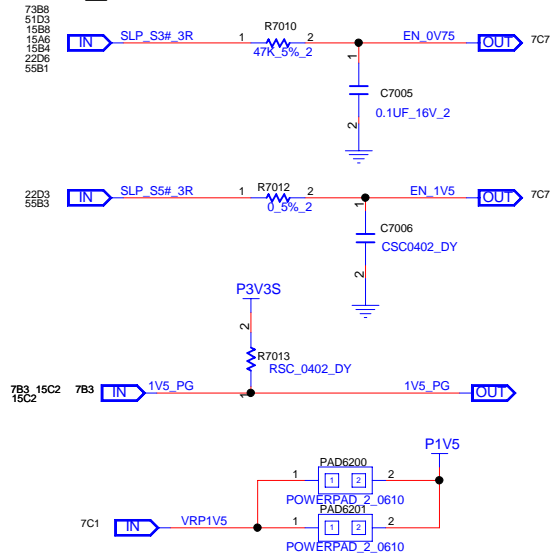
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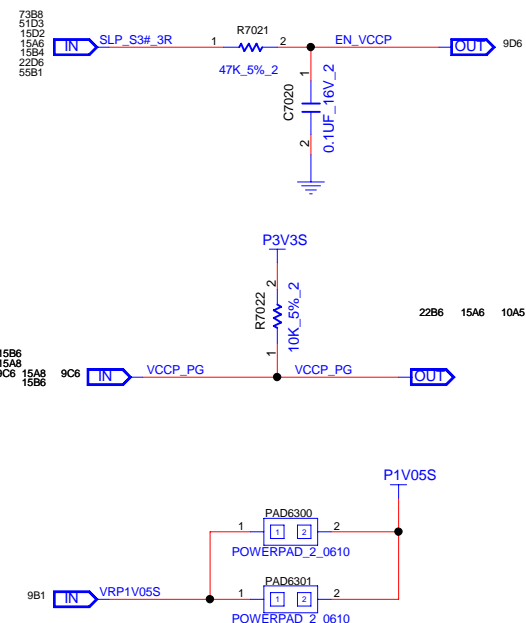
## 3V & 5V



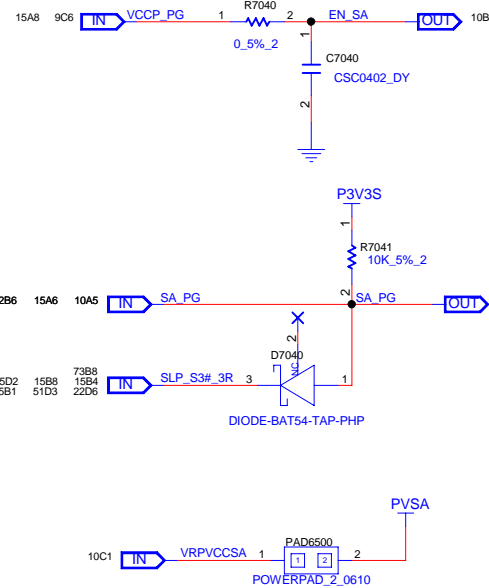
## DDR\_P1V5



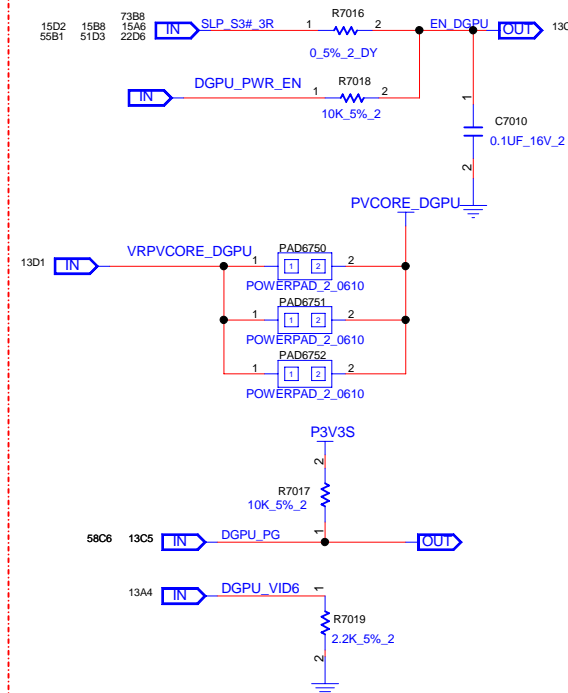
## VCCIO



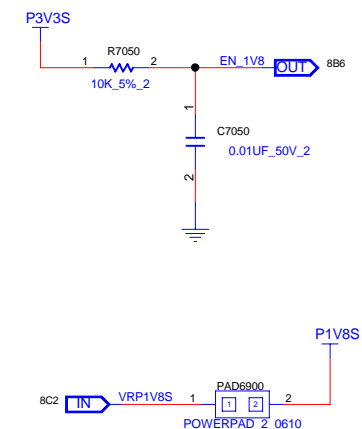
## VCCSA



## DGPU



## P1V8S



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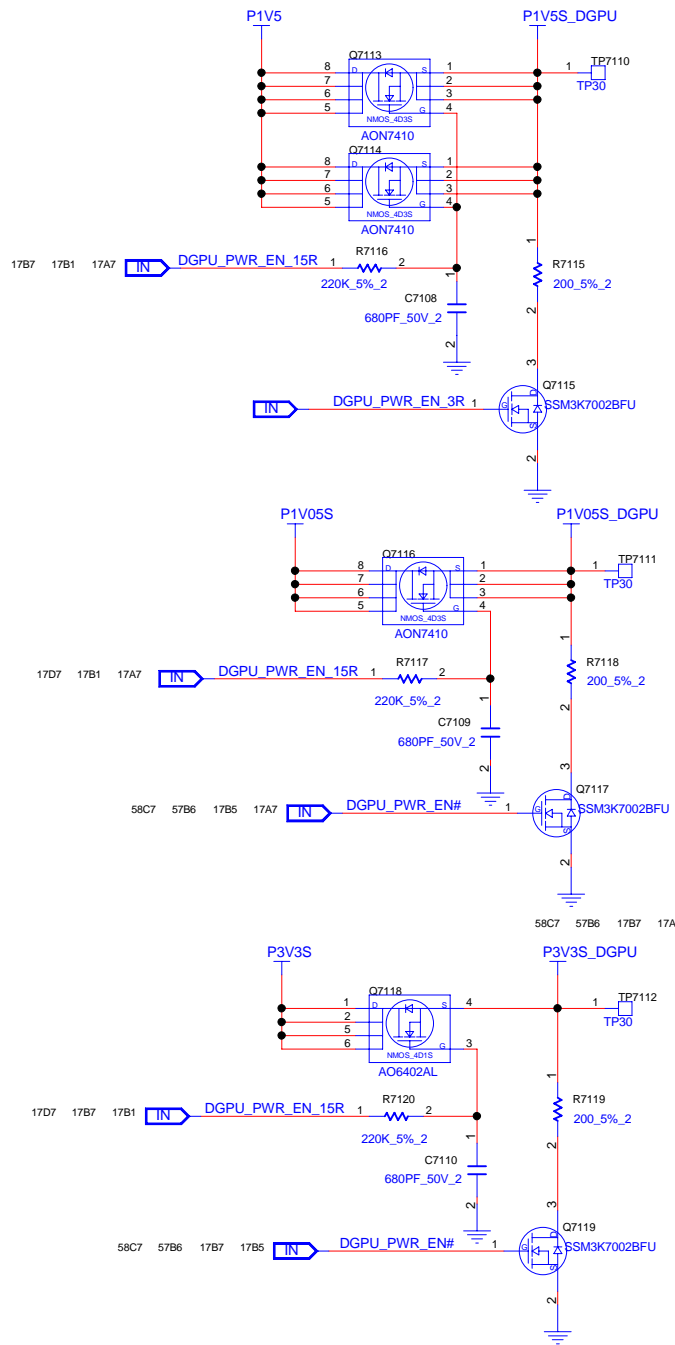
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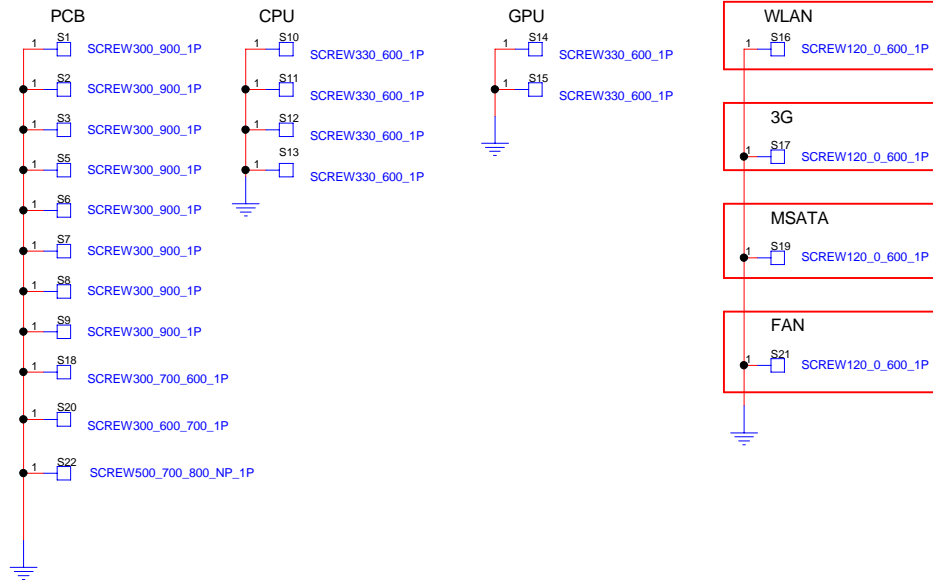
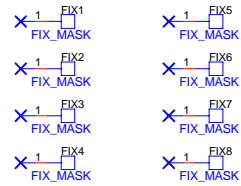
# NVIDIA OPTIMUS

	DURING RESET	AFTER RESET	
DGPU_PWR_EN#	HIGH	HIGH	0 : DGPU POWER SWITCH TURNED ON 1 : POWER SWITCH TURNED OFF
DGPU_PG			0 : DGPU POWER IS NOT STABLE 1 : DGPU POWER IS STABLE
DGPU_HOLD_RST#	LOW	LOW	0 : KEEP DGPU IN RESET 1 : RESET IS RELEASED

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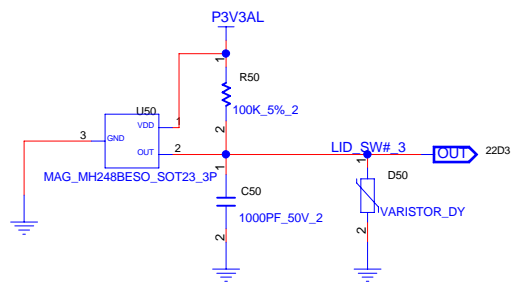
REFERENCE 0~49(PCB SCREW)



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REFERENCE 50-99(HALL SENSOR)



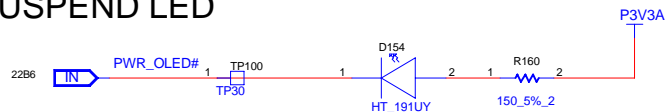
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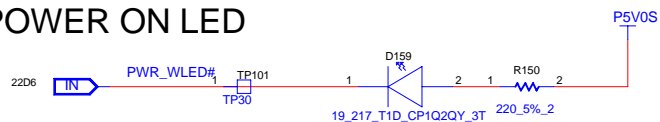
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## SUSPEND LED

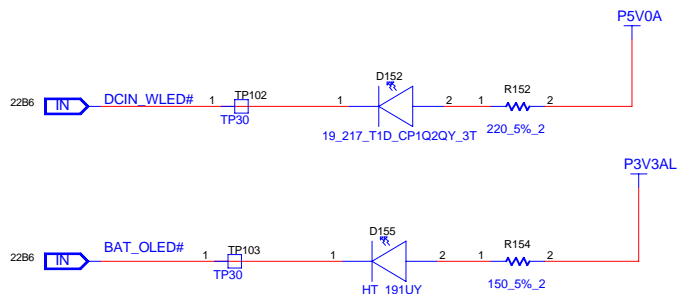


## POWER ON LED

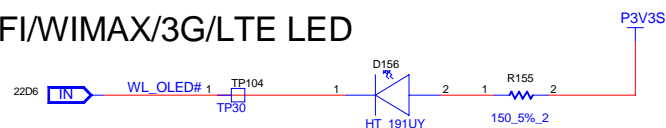


## DC IN / BATTERY CHARGE LED

D152 BRIGHT:BOTH AC-ADAPTER IS PLUGGED IN AND BATTERY IS FULL CHARGED  
D155 BRIGHT:WHILE CHARGING BATTERY FROM AC-ADAPTER  
BLINK:LOW BATTERY



## WIFI/WIMAX/3G/LTE LED



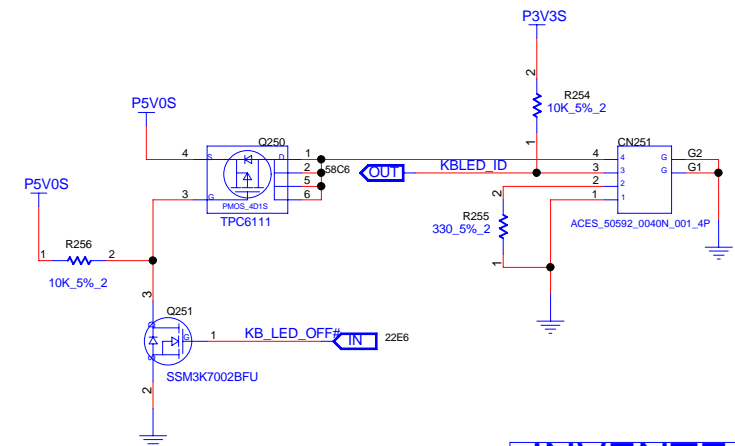
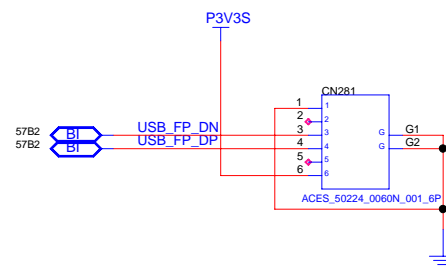
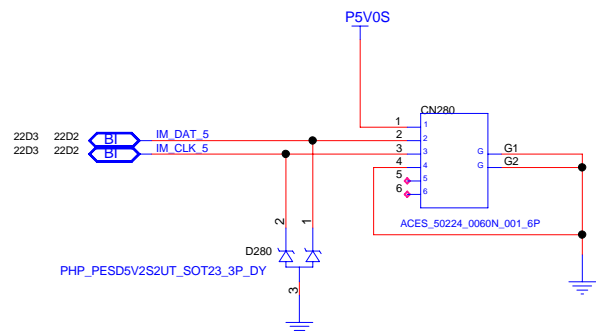
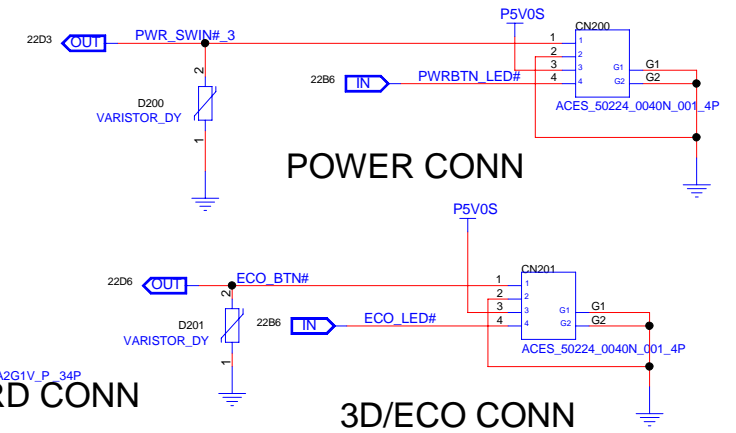
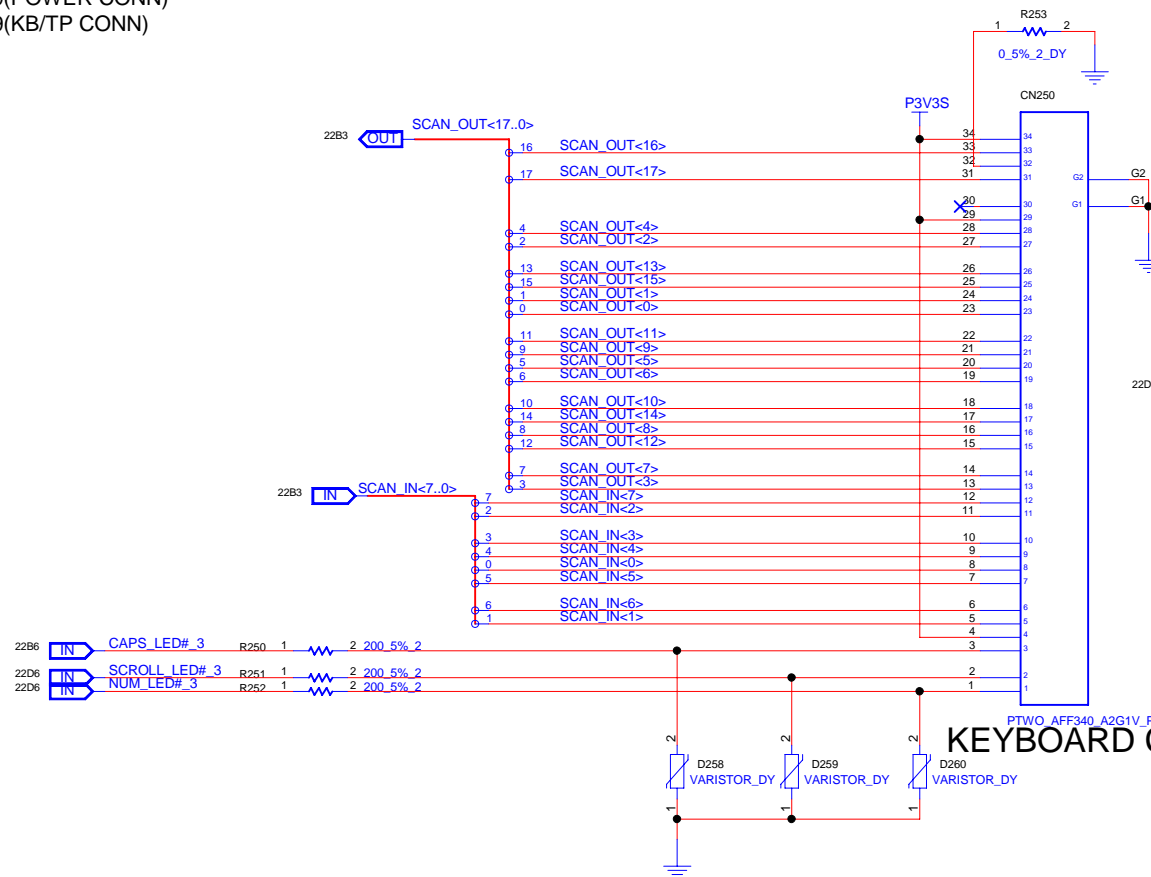
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REFERENCE 200~249(POWER CONN)  
REFERENCE 250~299(KB/TP CONN)



TOUCHPAD CONN

FP CONN

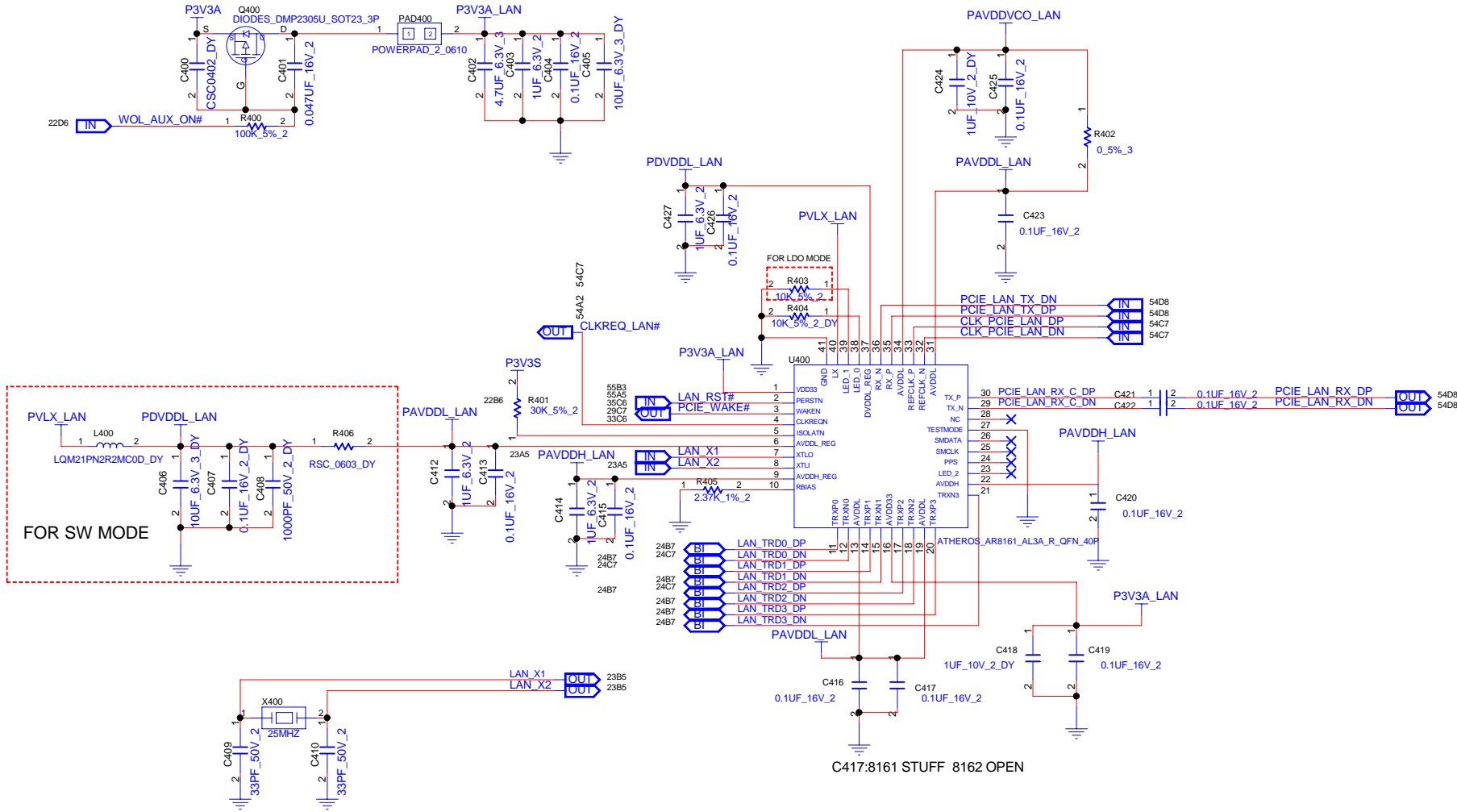
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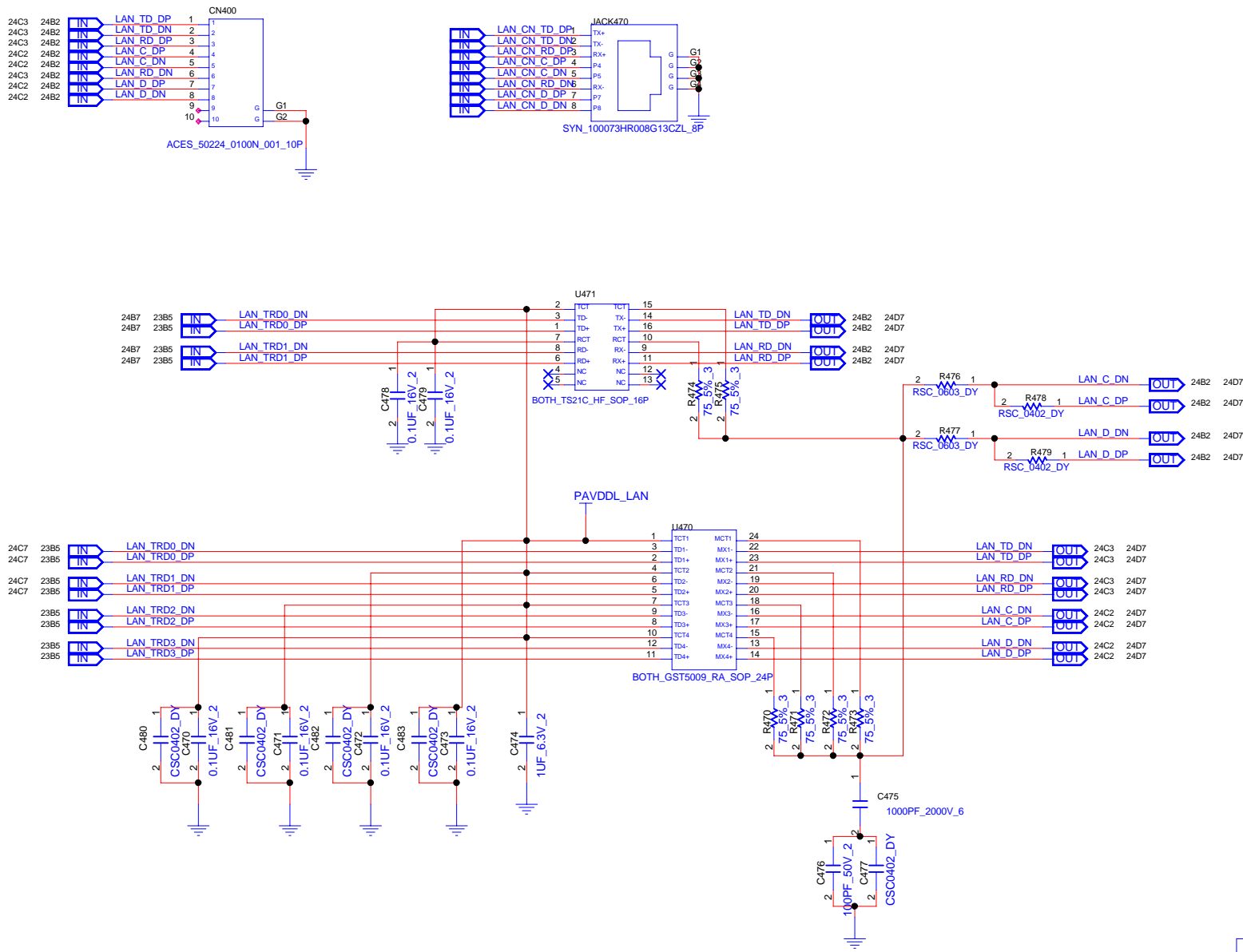




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A3	CS		

REFERENCE 400~499(LAN)



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CODE		REV	
X01		X01	



REFERENCE 500~549(AUDIO CODEC)

D

C

B

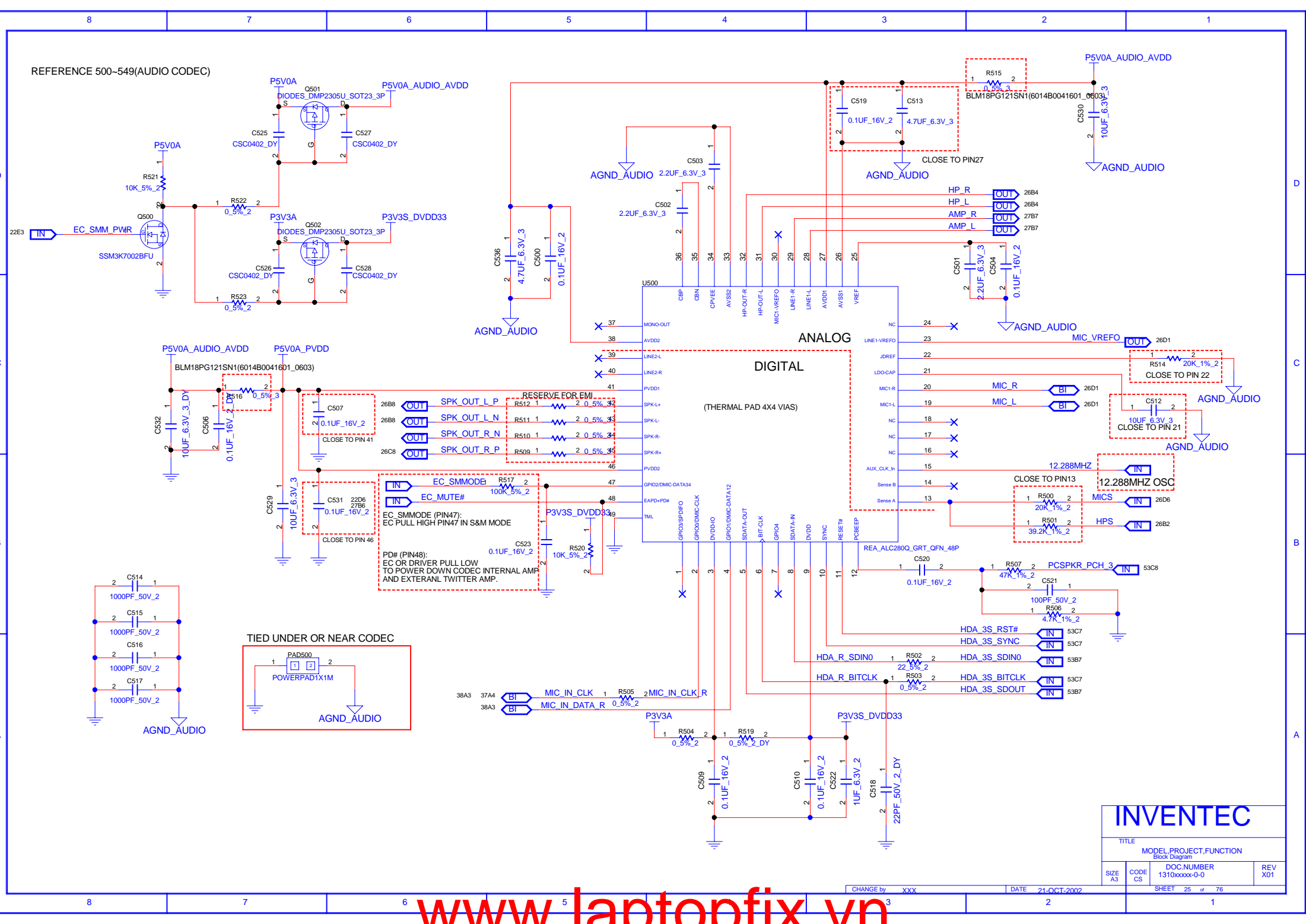
A

D

C

B

A



INVENTEC

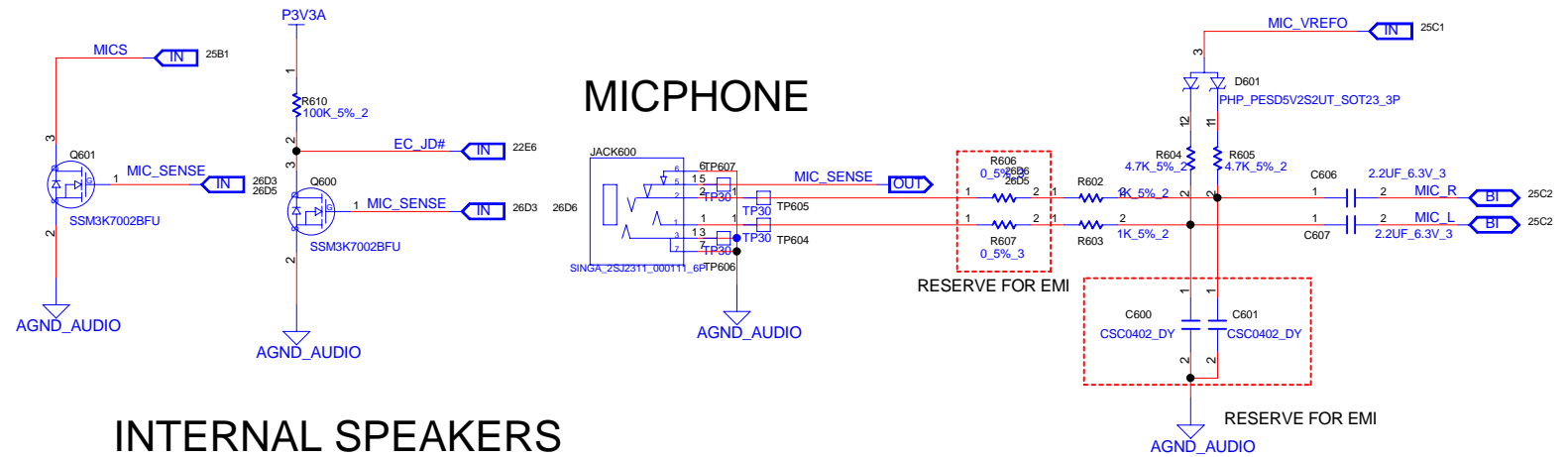
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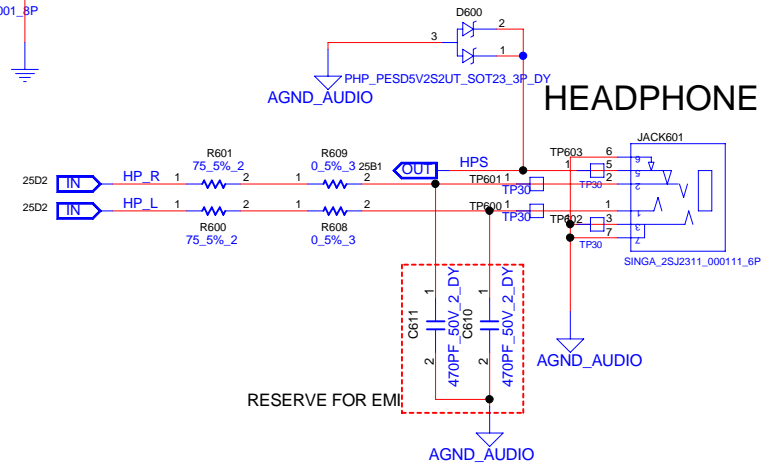
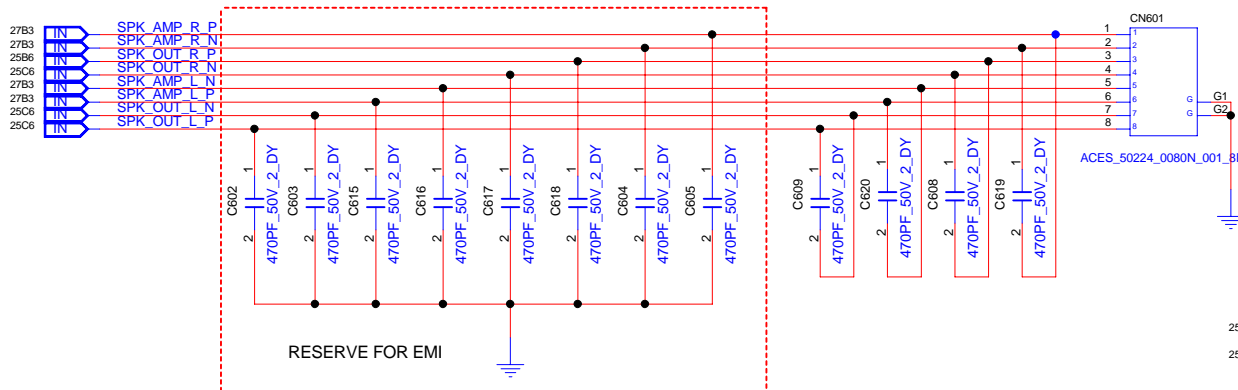
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# AUDIO JACKS



## INTERNAL SPEAKERS

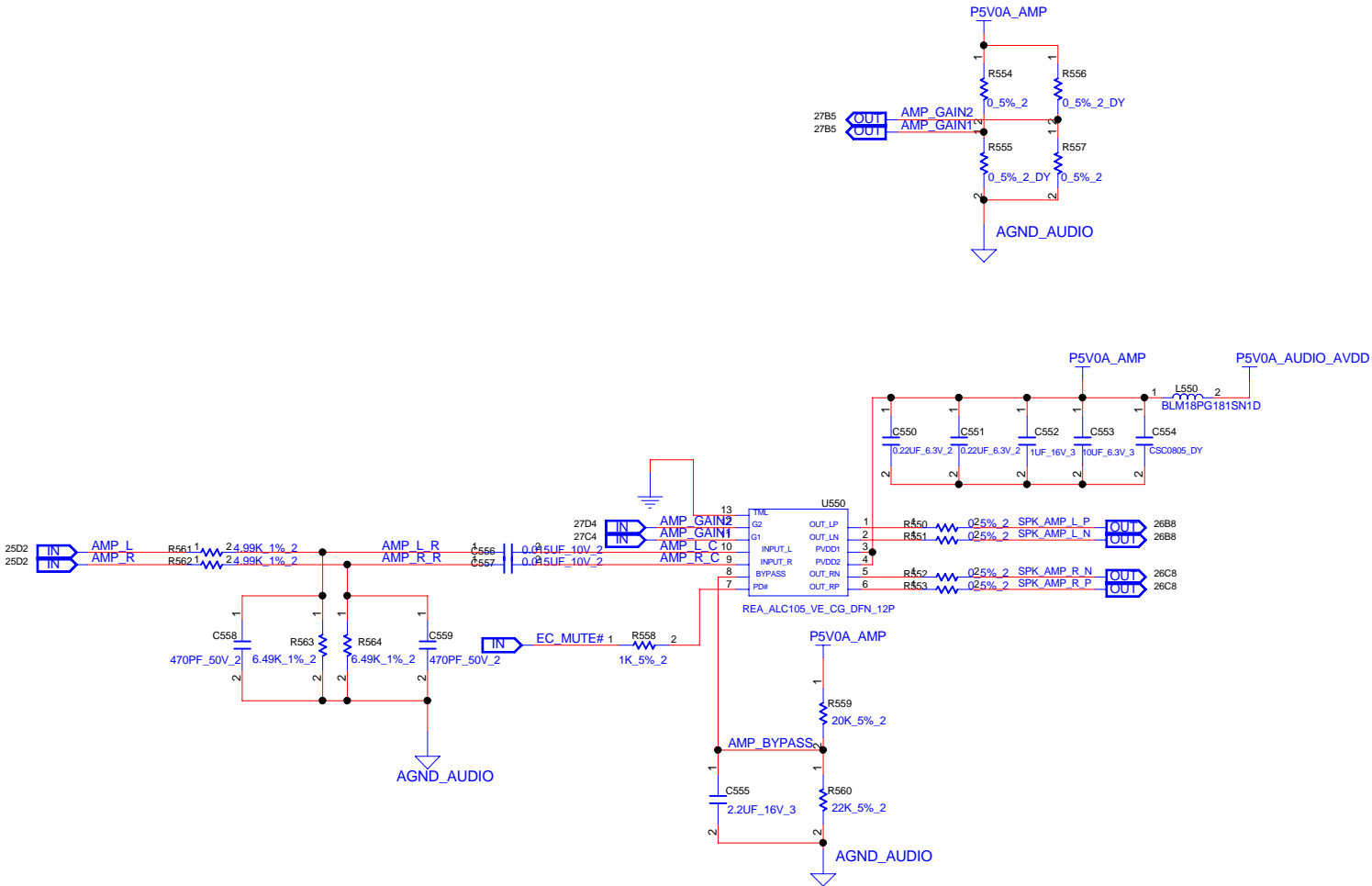
NOTE:SPK TRACE SHOULD 30~40 MILS WIDTH



INVENTEC

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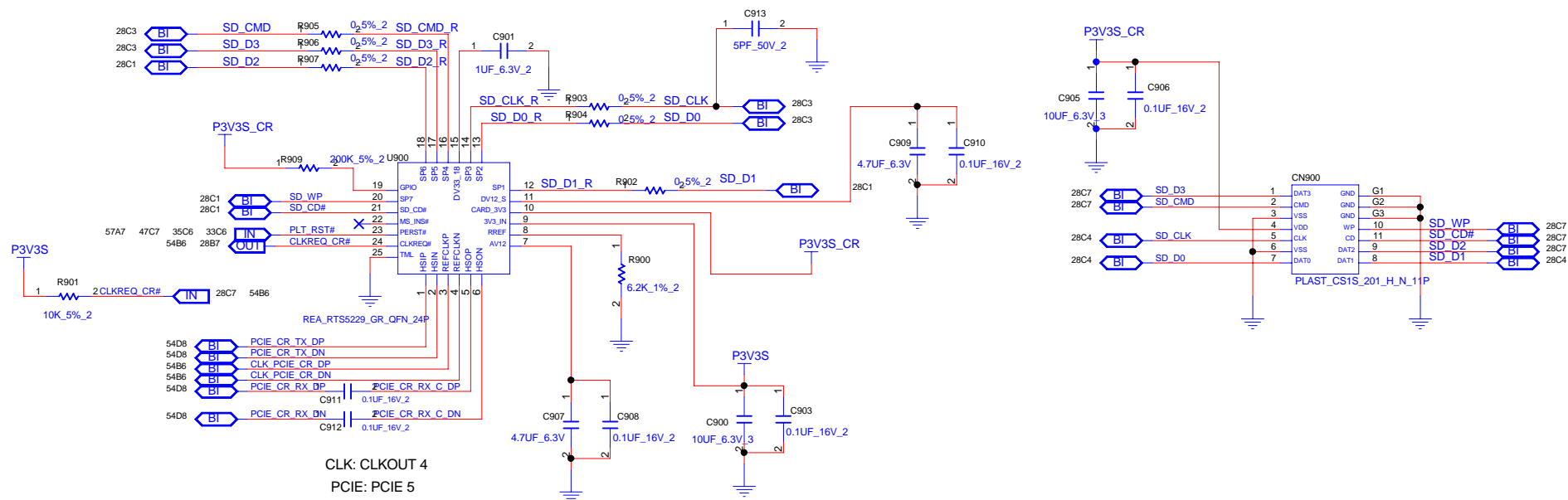
GAIN SETTING		
G2	G1	AMP
0	0	11DB
0	1	14DB
1	0	19DB
1	1	25DB



INVENTEC

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REFERNCE 900~999(CARDREADER)



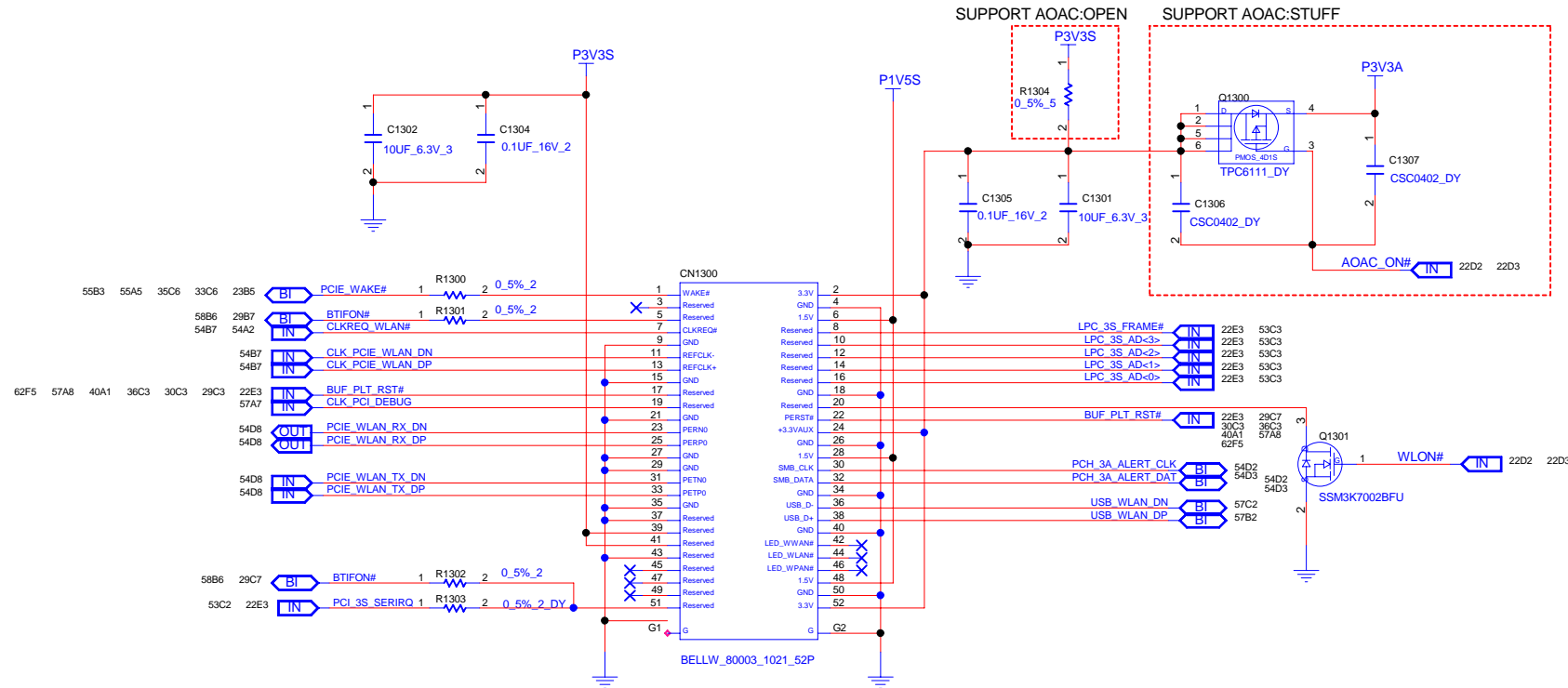
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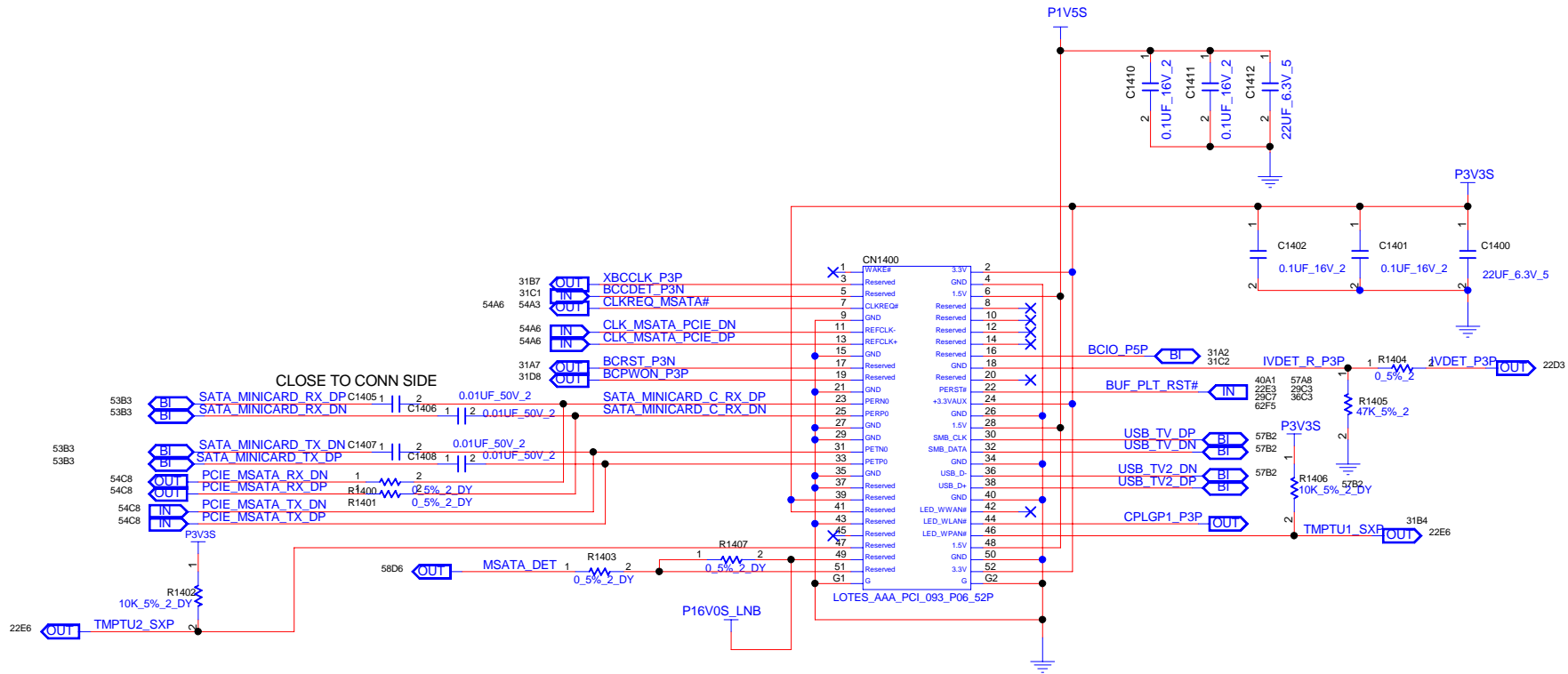


MINI CARD 1(WLAN)

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REFERENCE 1400~1499(3G)



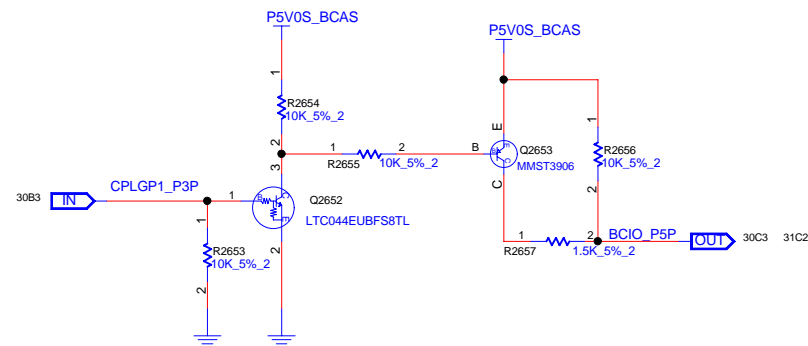
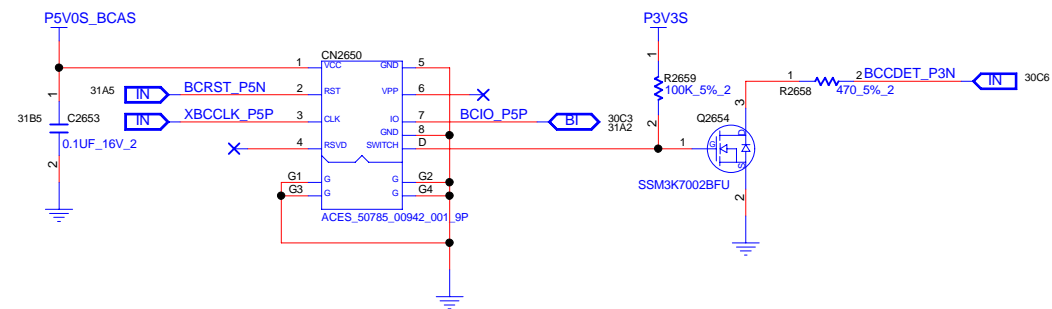
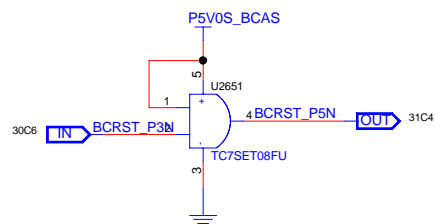
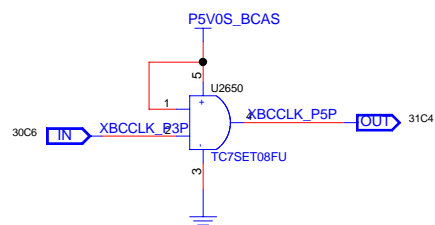
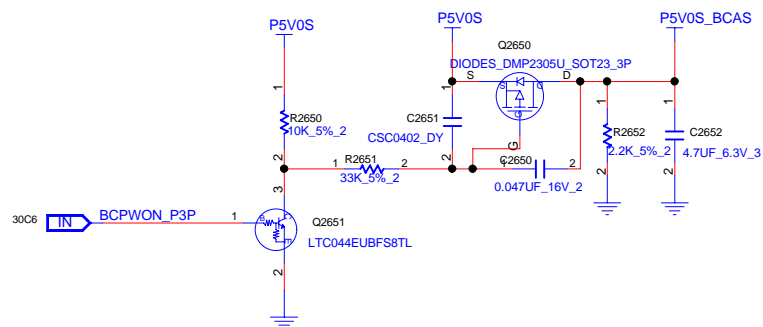
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REFERENCE 2650~2999(RESERVE)



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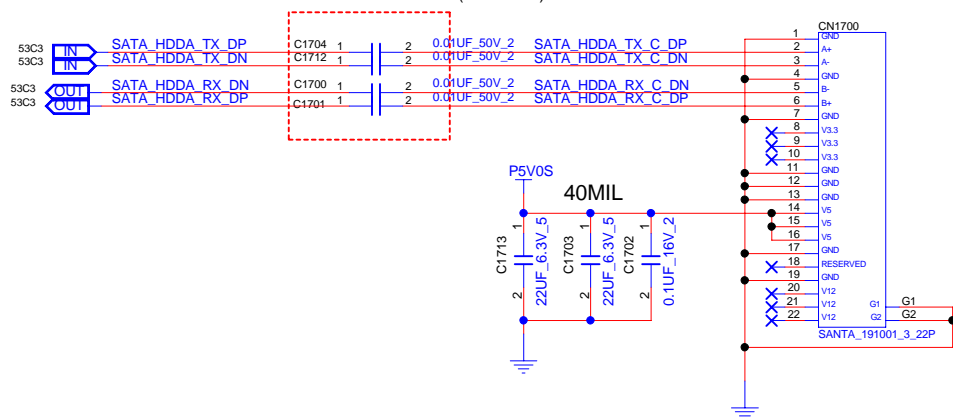
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REFERENCE 1700~1749(HDD)  
REFERENCE 1750~1799(ODD)

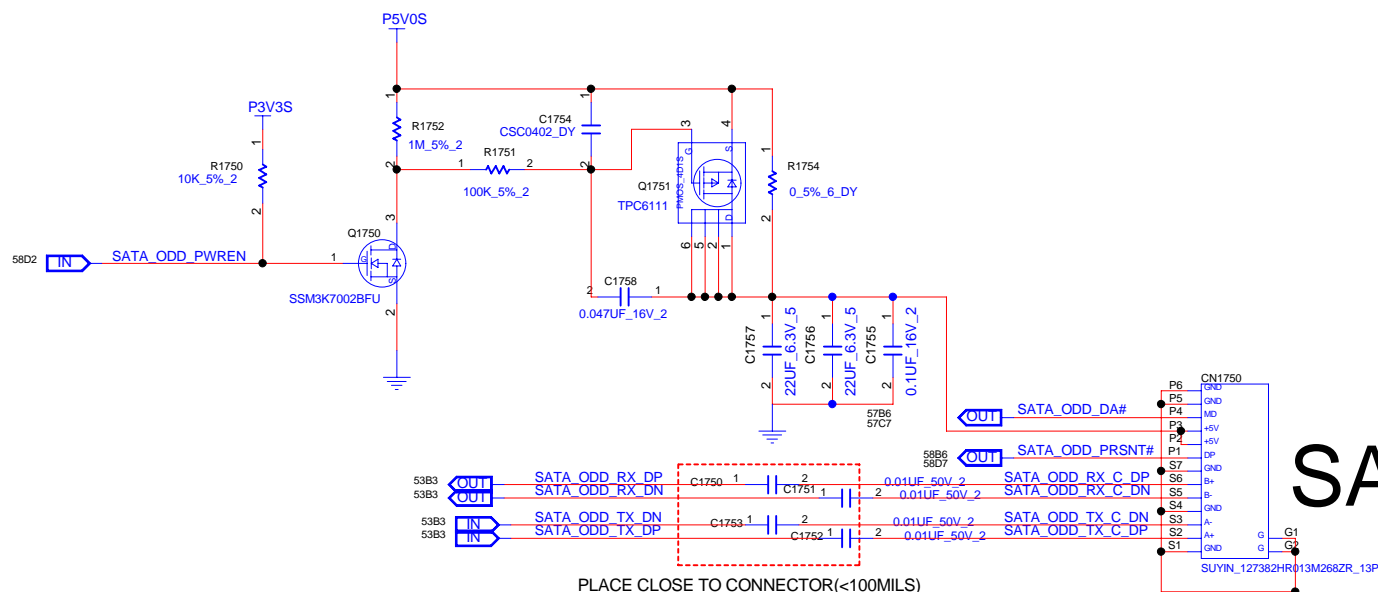
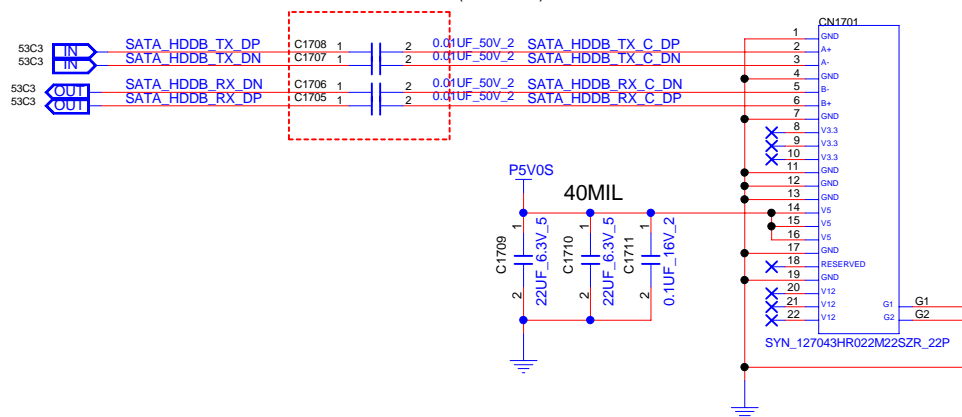
# SATA HDD A

# SATA HDD B

PLACE CLOSE TO CONNECTOR(<100MILS)



PLACE CLOSE TO CONNECTOR(<100MILS)



PLACE CLOSE TO CONNECTOR(<100MILS)

# SATA ODD

INVENTEC

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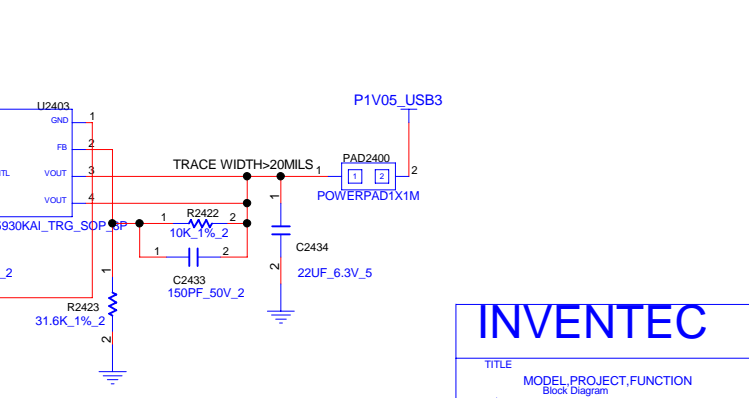
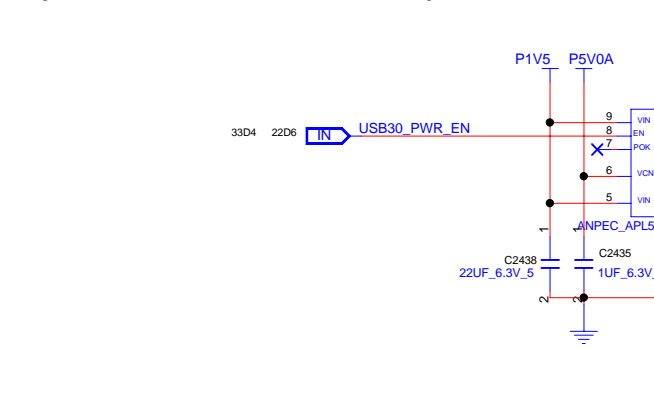
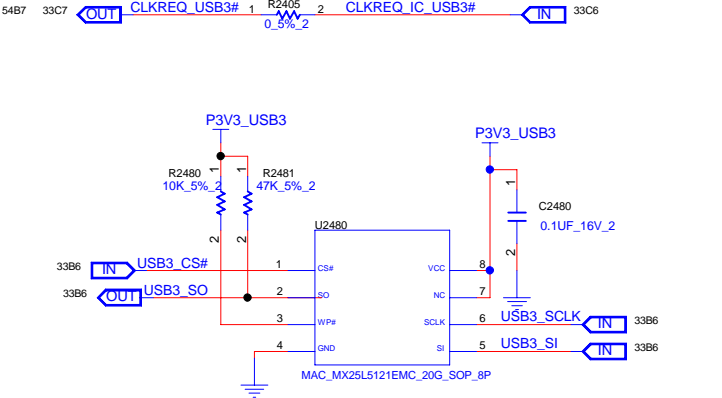
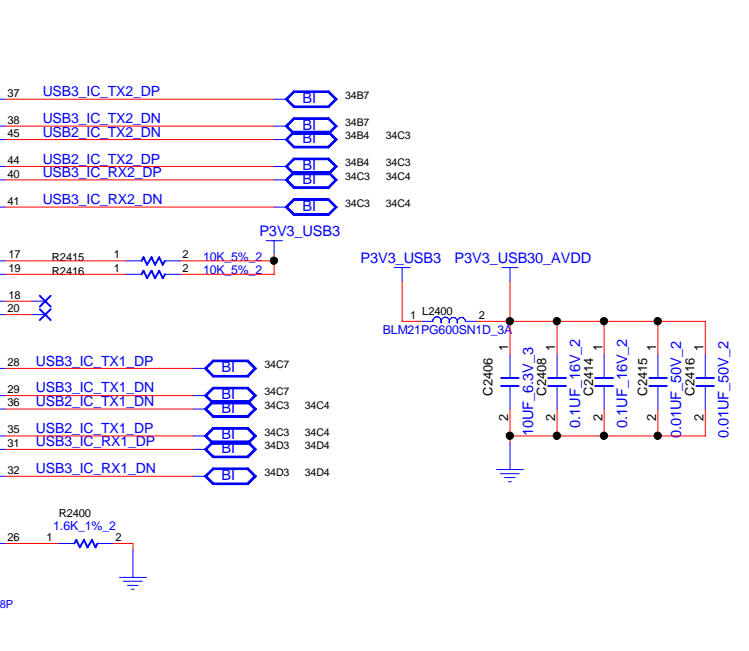
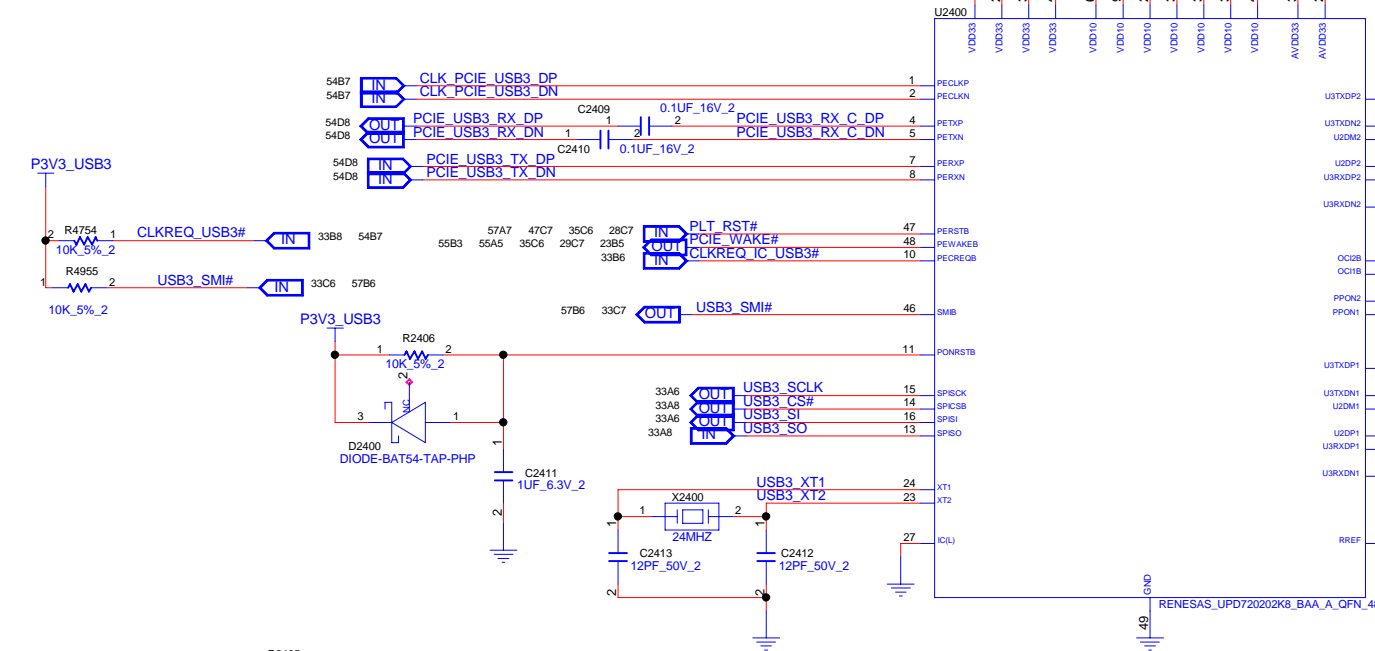
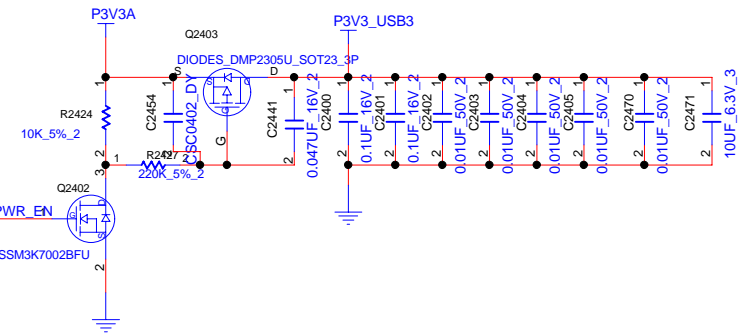
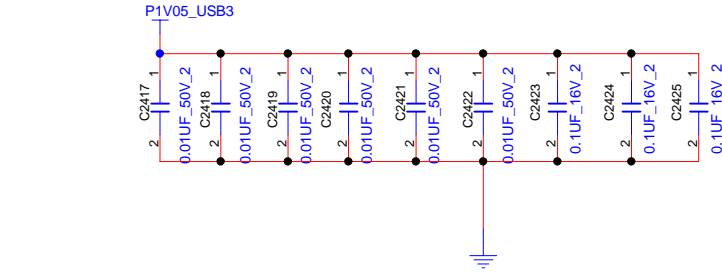
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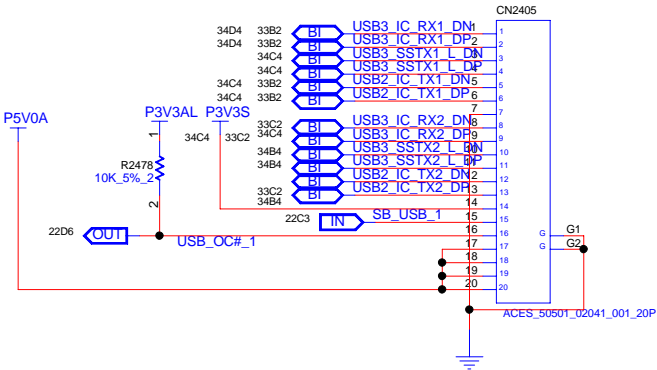
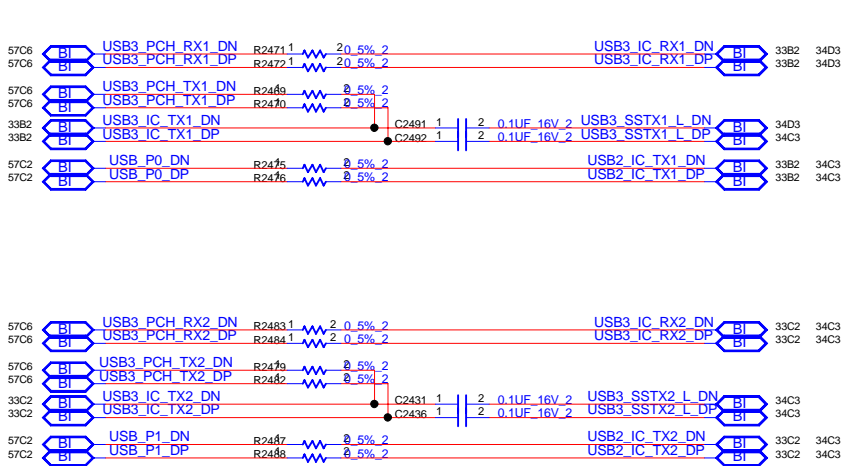


# REFERENCE 2000~2099(USB)



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REFERENCE 2400~2499(USB3.0)



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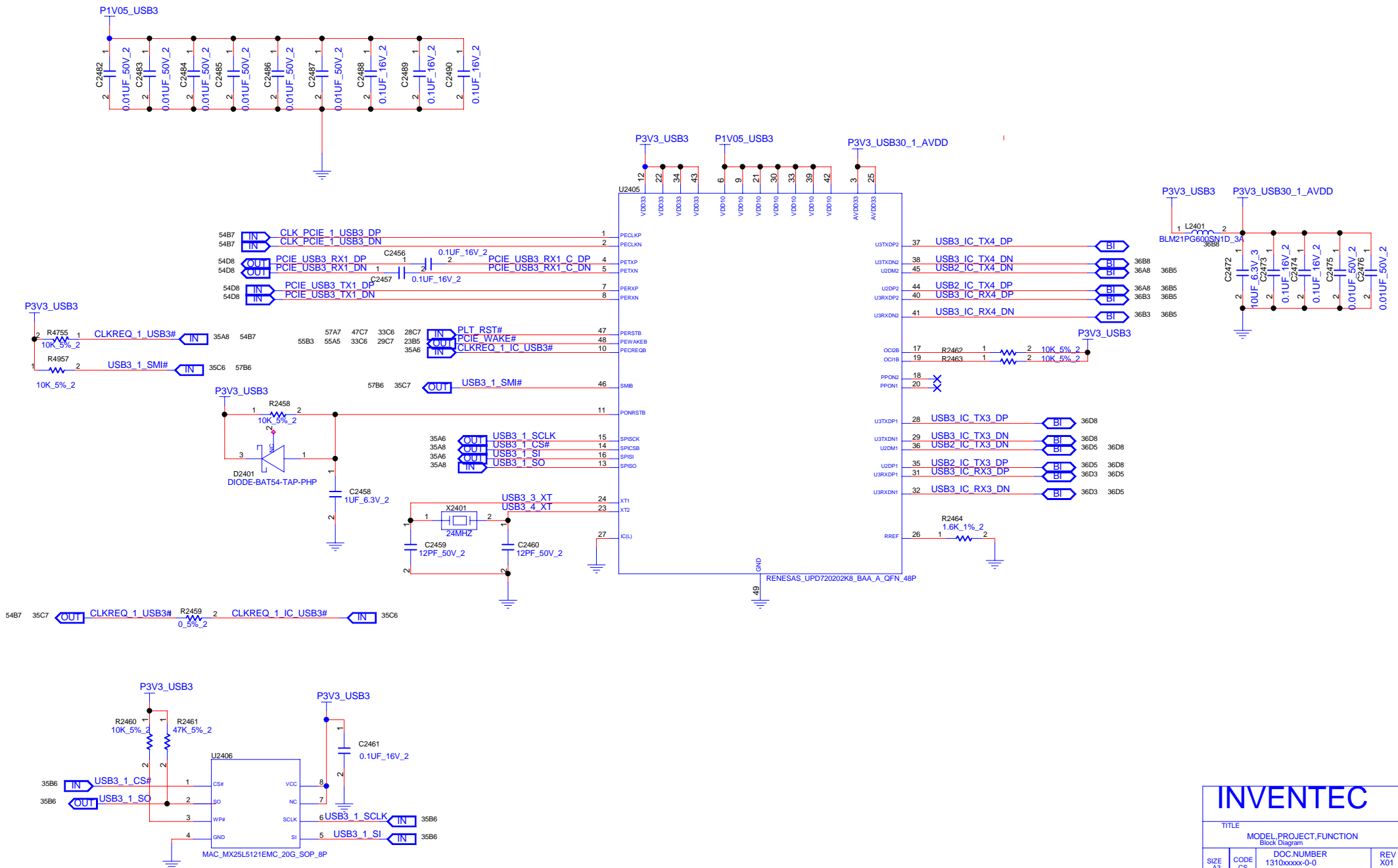
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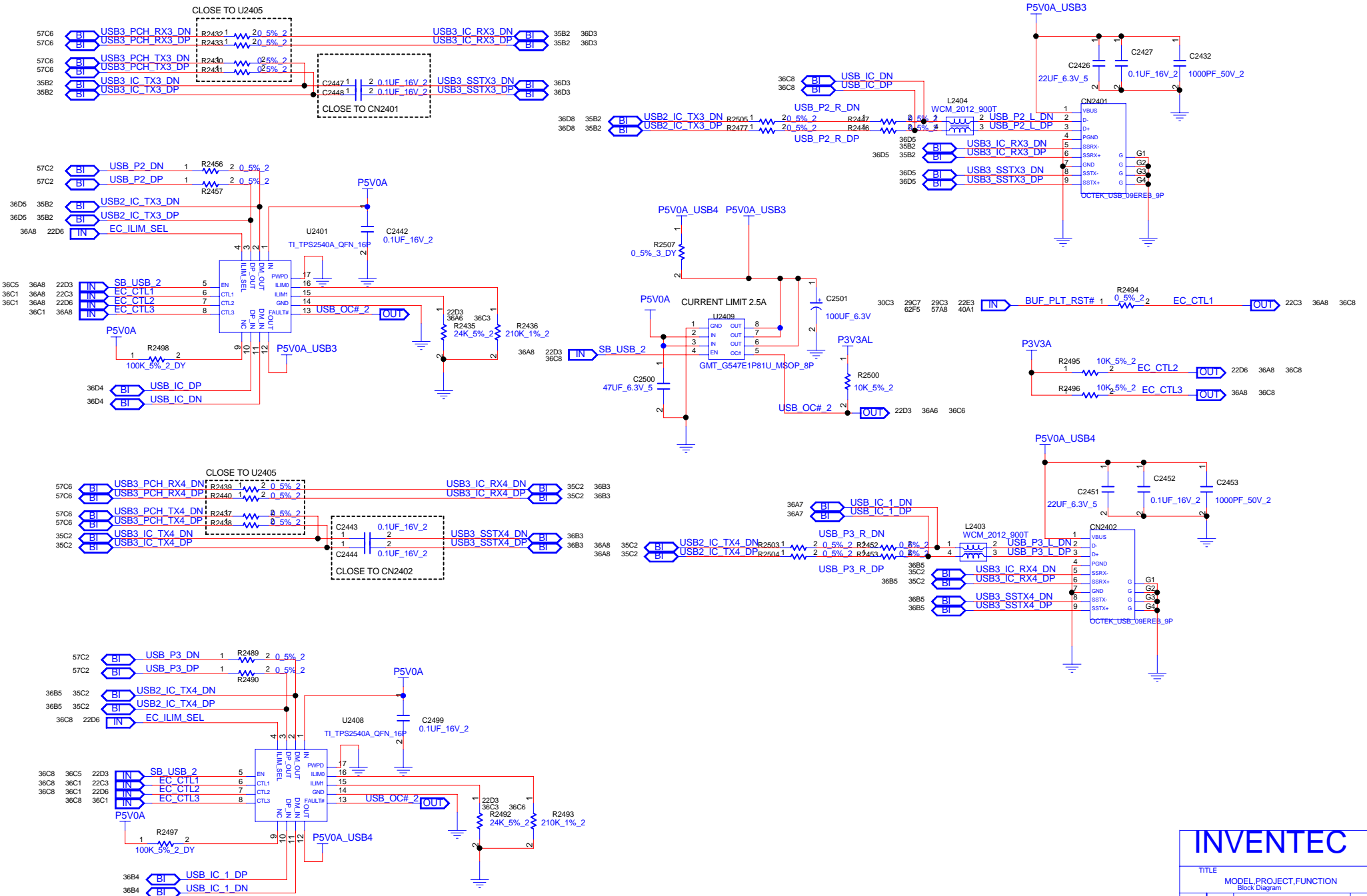
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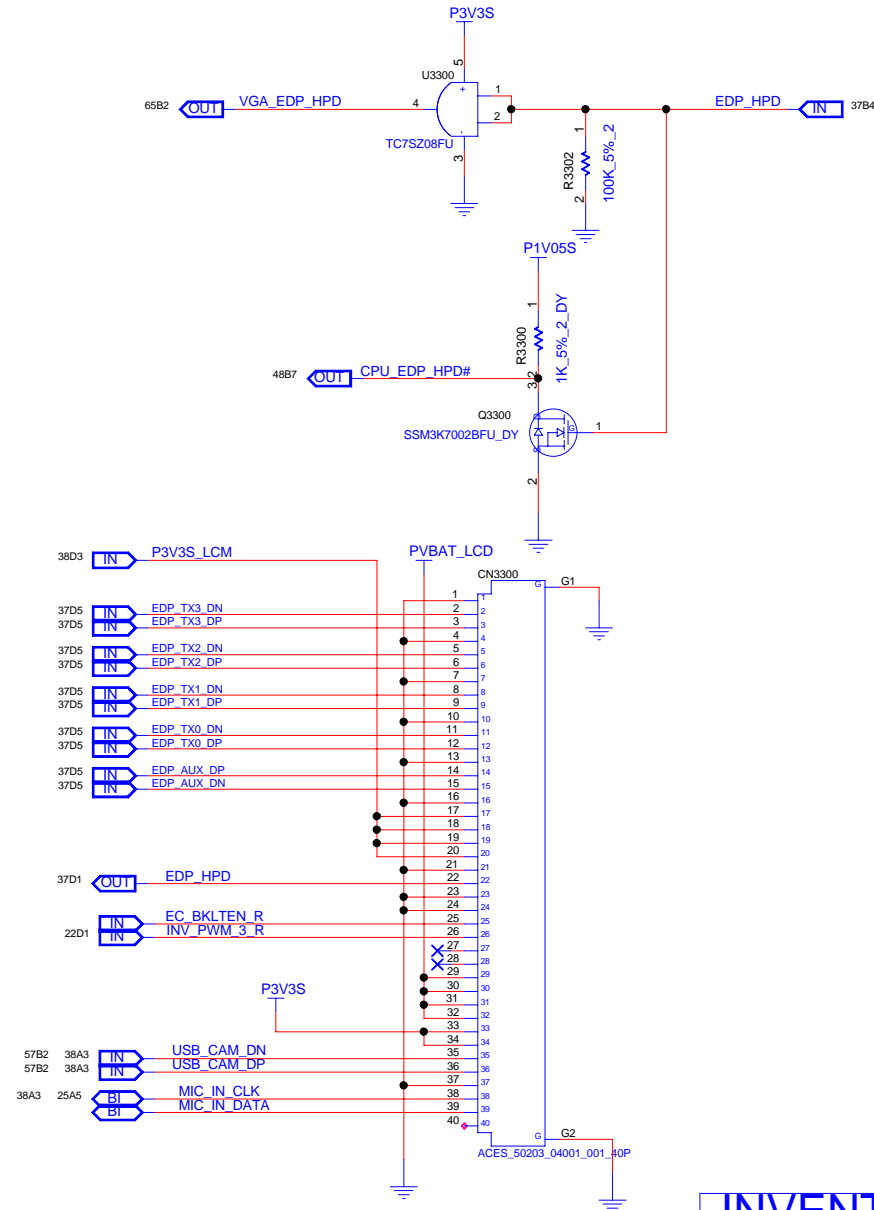
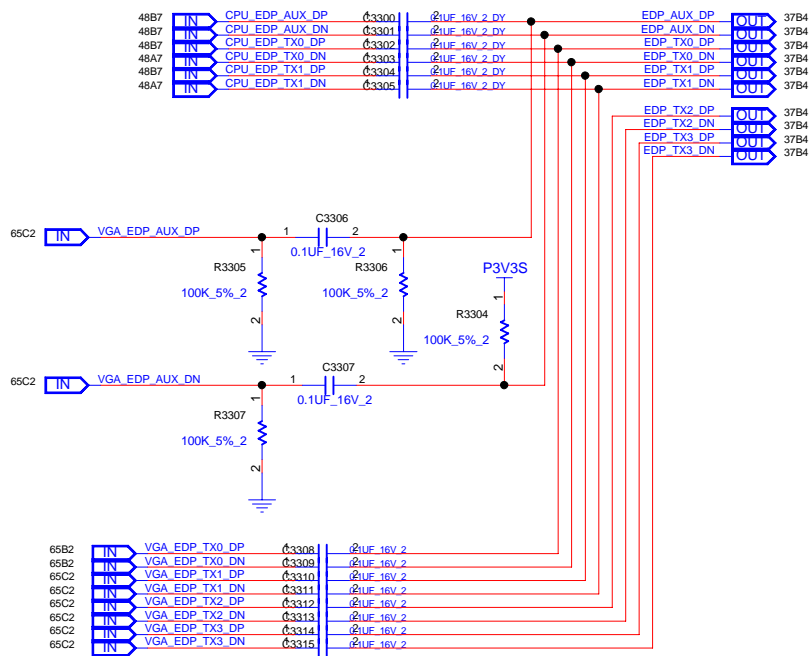
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CLOSE TO U2405



TITLE			
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X01

SIZE A3

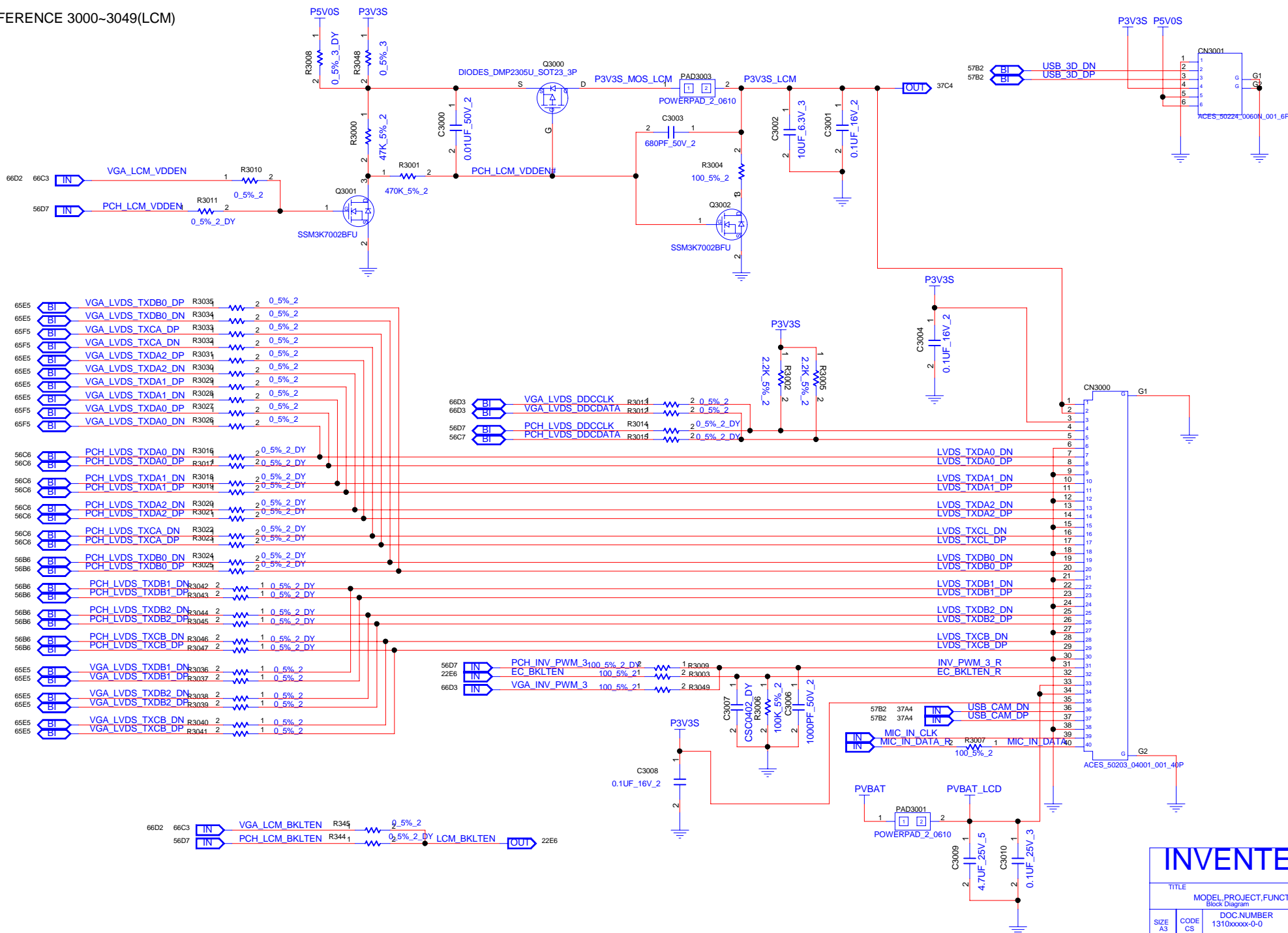
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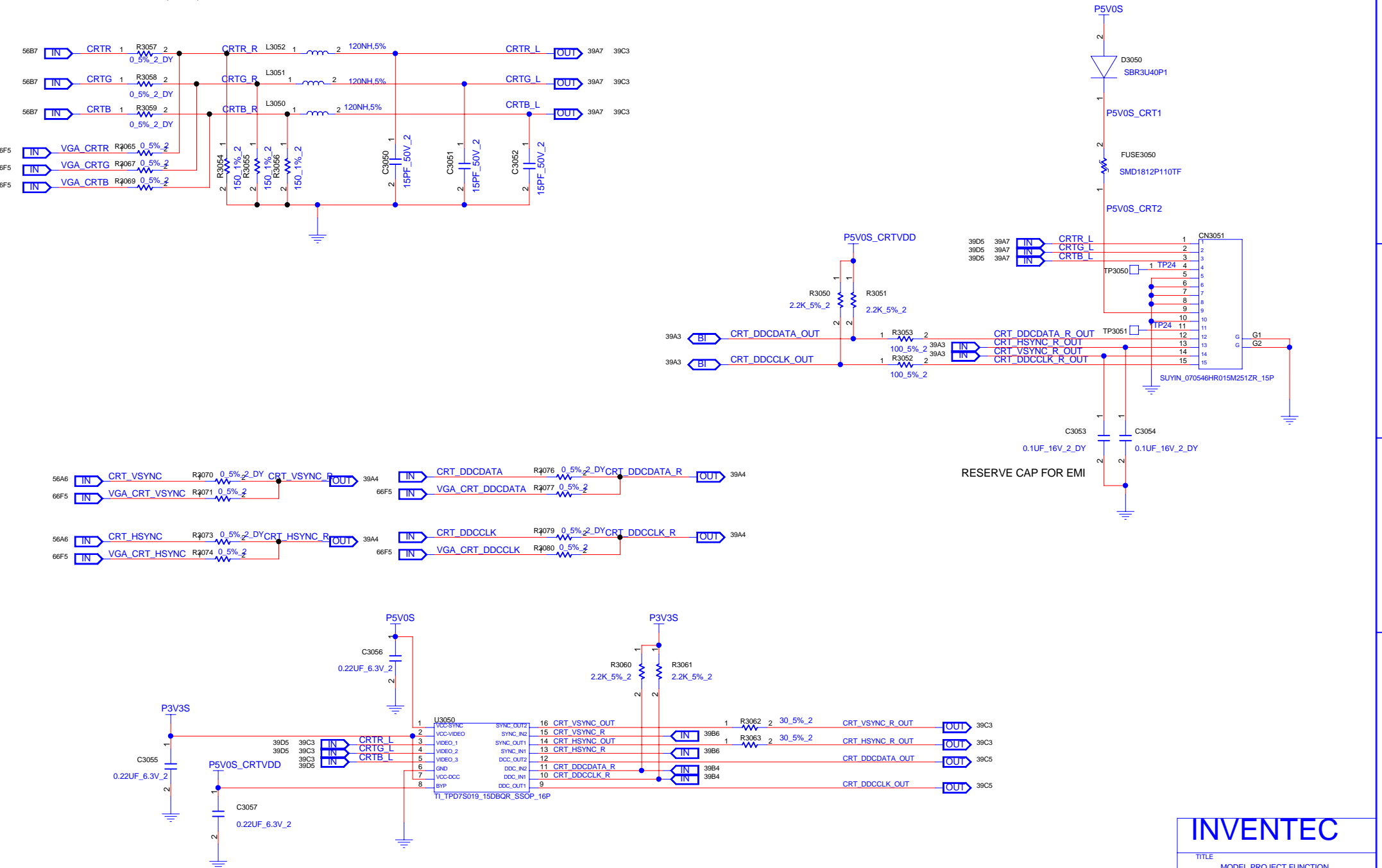
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SHEET 38 of 76

# REFERENCE 3050~3099(CRT)



INVENTEC

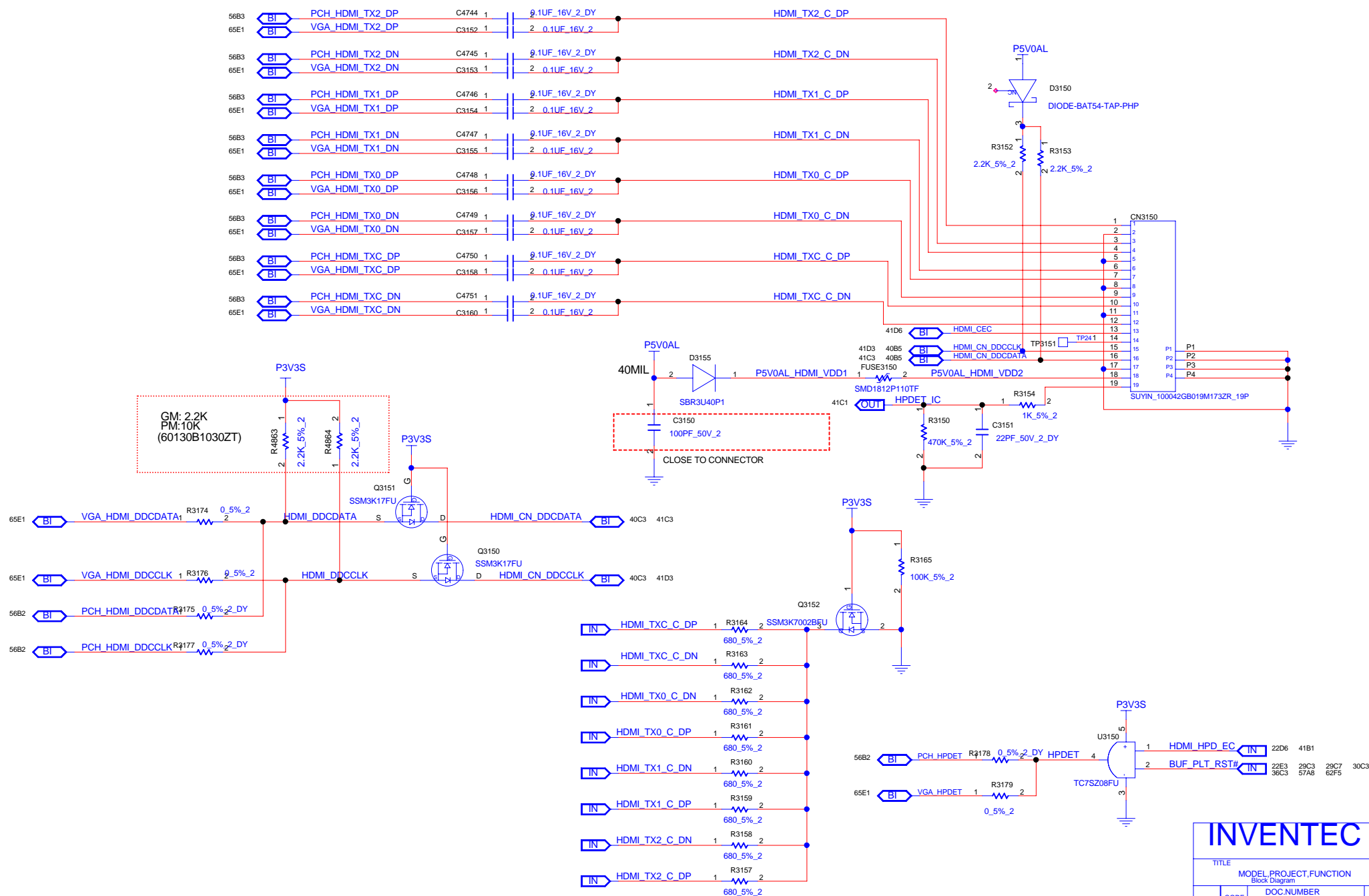
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

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PLACE CLOSE TO CONNECTOR



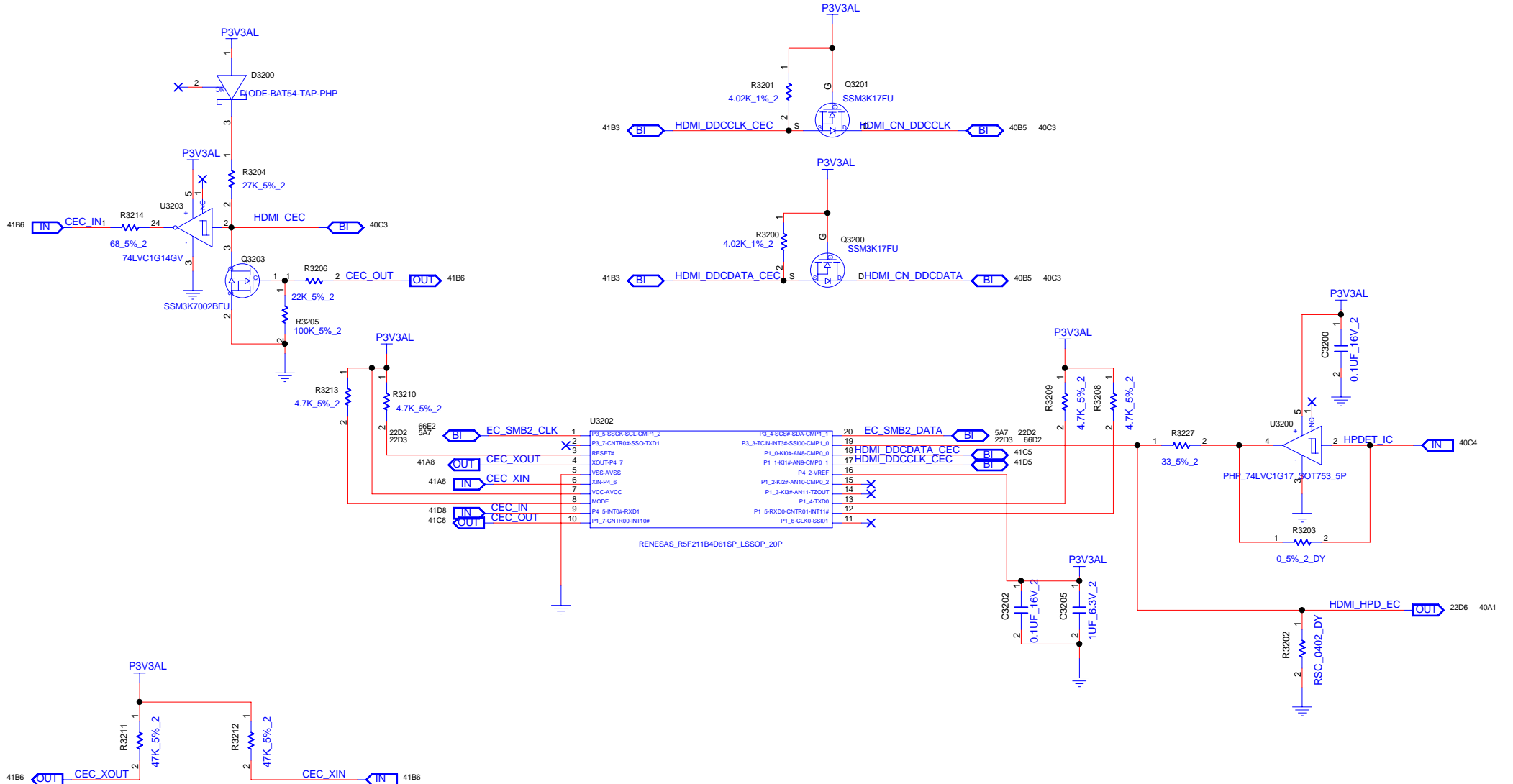
INVENTEC

TITLE		
MODEL,PROJECT,FUNCTION Block Diagram		
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0

CHANGE by	XXX	DATE	21-OCT-2002
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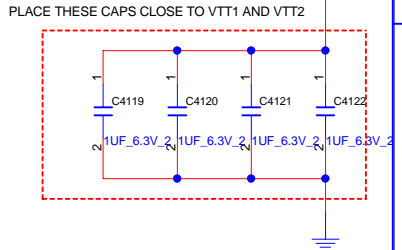


INVENTEC

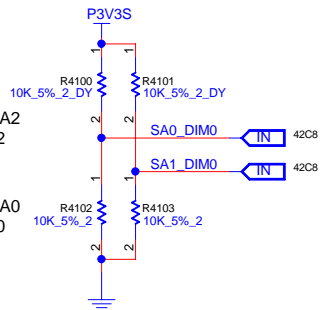
TITLE  
MODEL PROJECT FUNCTION  
Block Diagram  
DOC NUMBER  
1310xxxxx-0-0  
REV  
X01

CHANGE by XXXX DATE 21-OCT-2002

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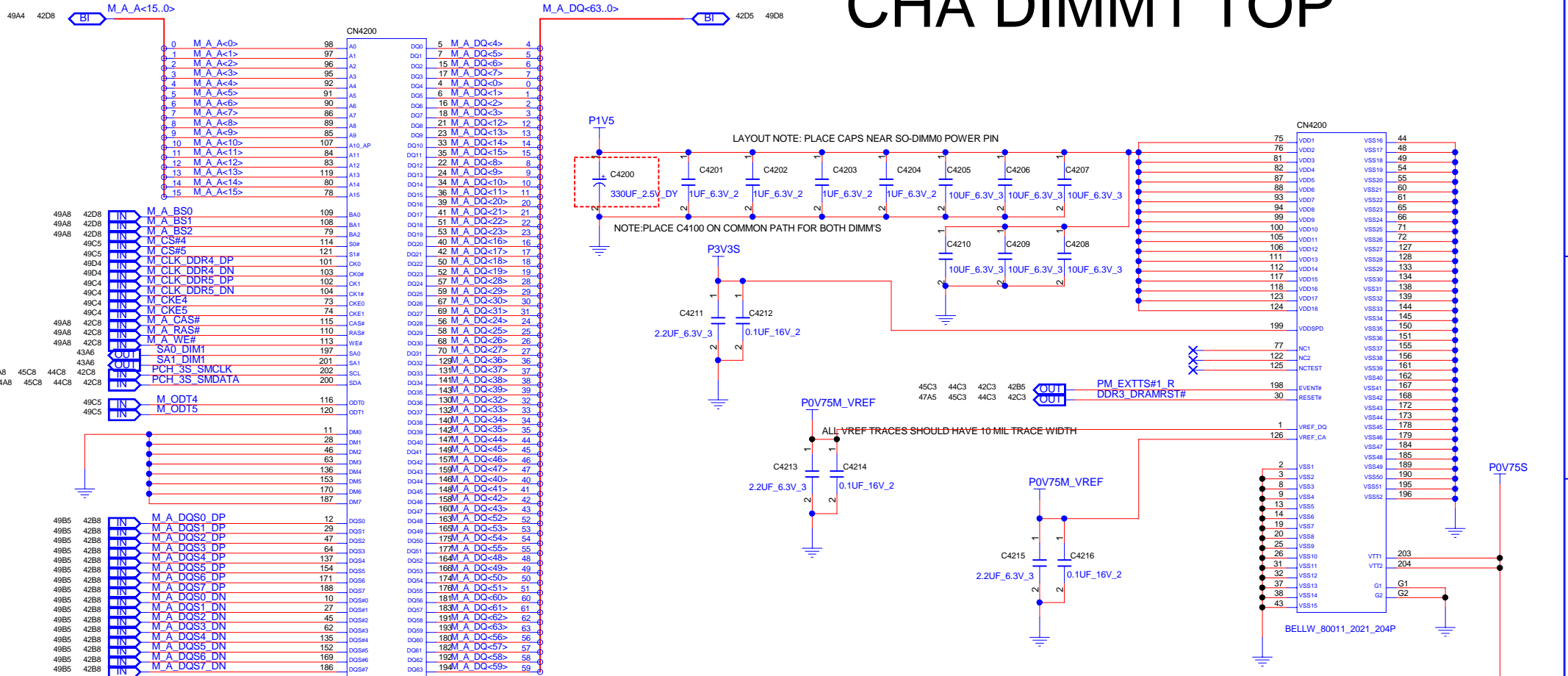
IF SA0\_DIM0=0 , SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0XA0  
SO-DIMMA TS ADDRESS IS 0X30



SIZE A3	CODE CS	1310xxxxx-0-0	
		SHEET 42 of 76	

REFERENCE 4200~4299(DDR)

## CHA DIMM1 TOP



BELLW\_80011\_2021\_204P

NOTE:

IF SA0\_DIM0=1, SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0XA2  
SO-DIMMA TS ADDRESS IS 0X32

IF SA0\_DIM0=0, SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0XA0  
SO-DIMMA TS ADDRESS IS 0X30

INVENTEC

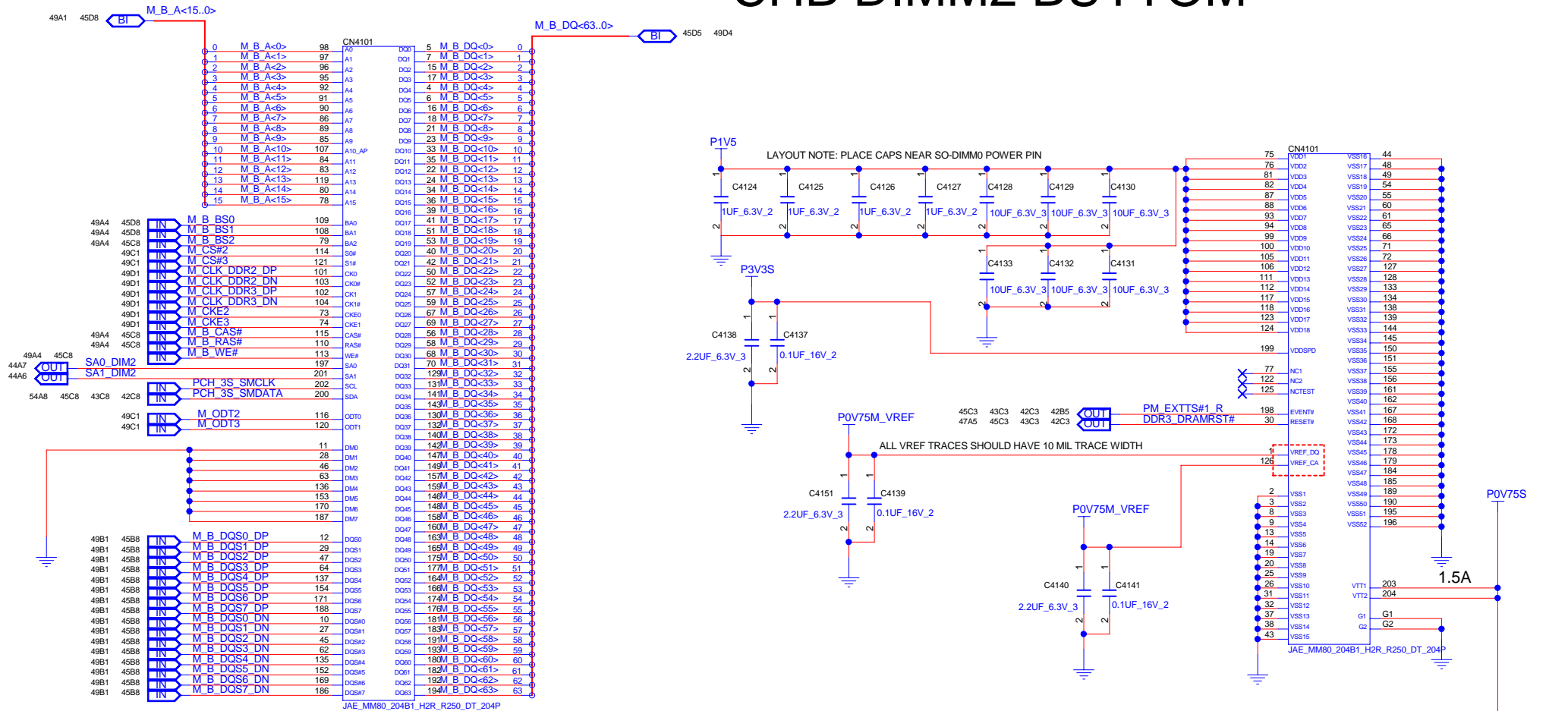
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX

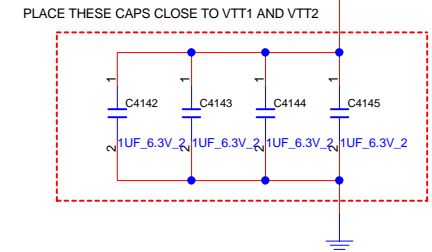
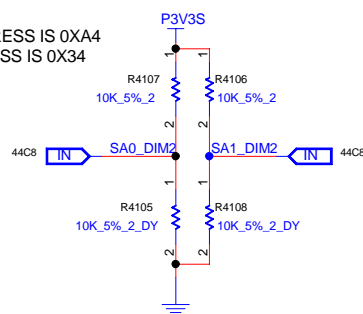
DATE 21-OCT-2002

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## CHB DIMM2 BUTTOM



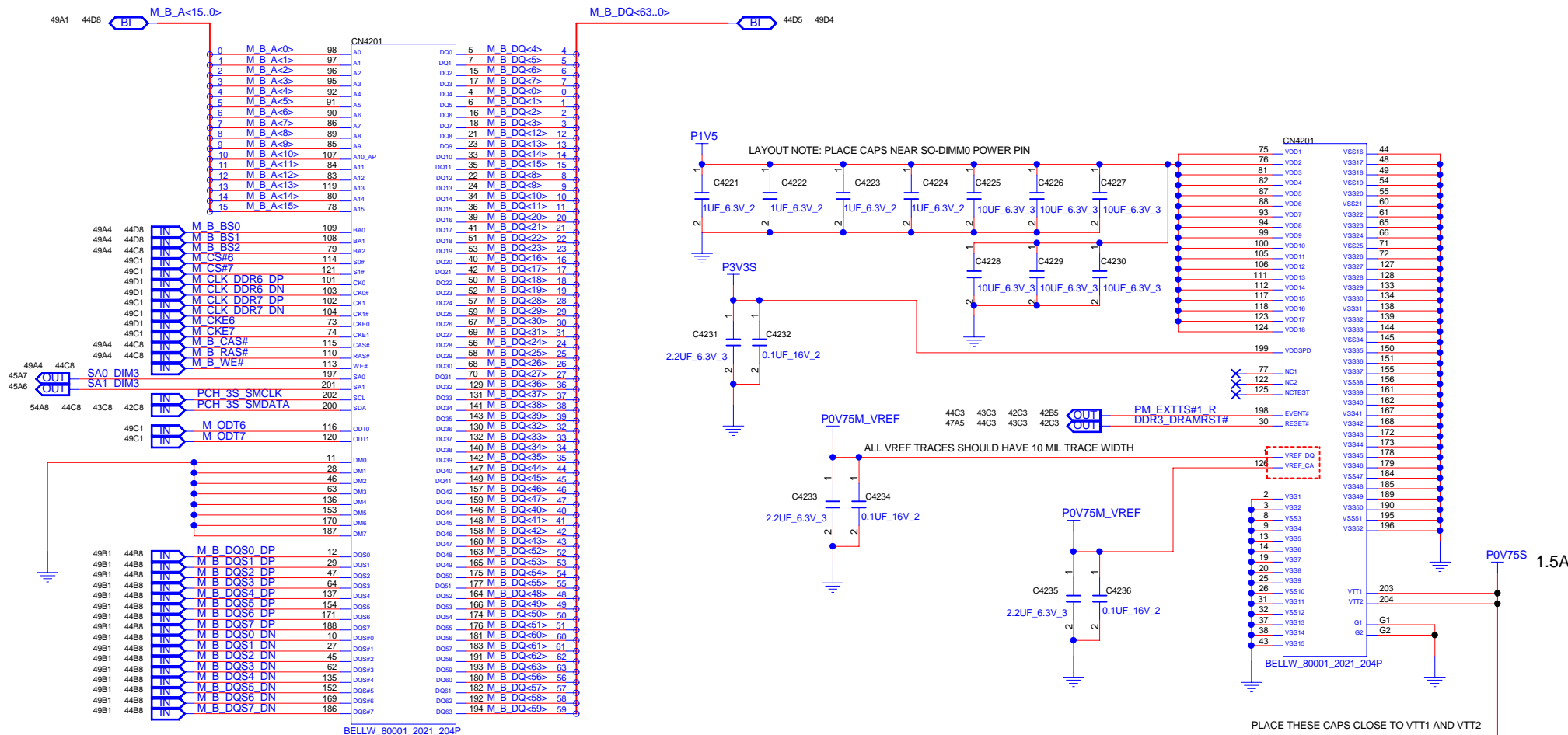
NOTE:  
SO-DIMMB SPD ADDRESS IS 0XA4  
SO-DIMMB TS ADDRESS IS 0X34



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxxx-0-0			
SIZE	CODE	REV	
A3	CS	X01	

## CHB DIMM3 TOP

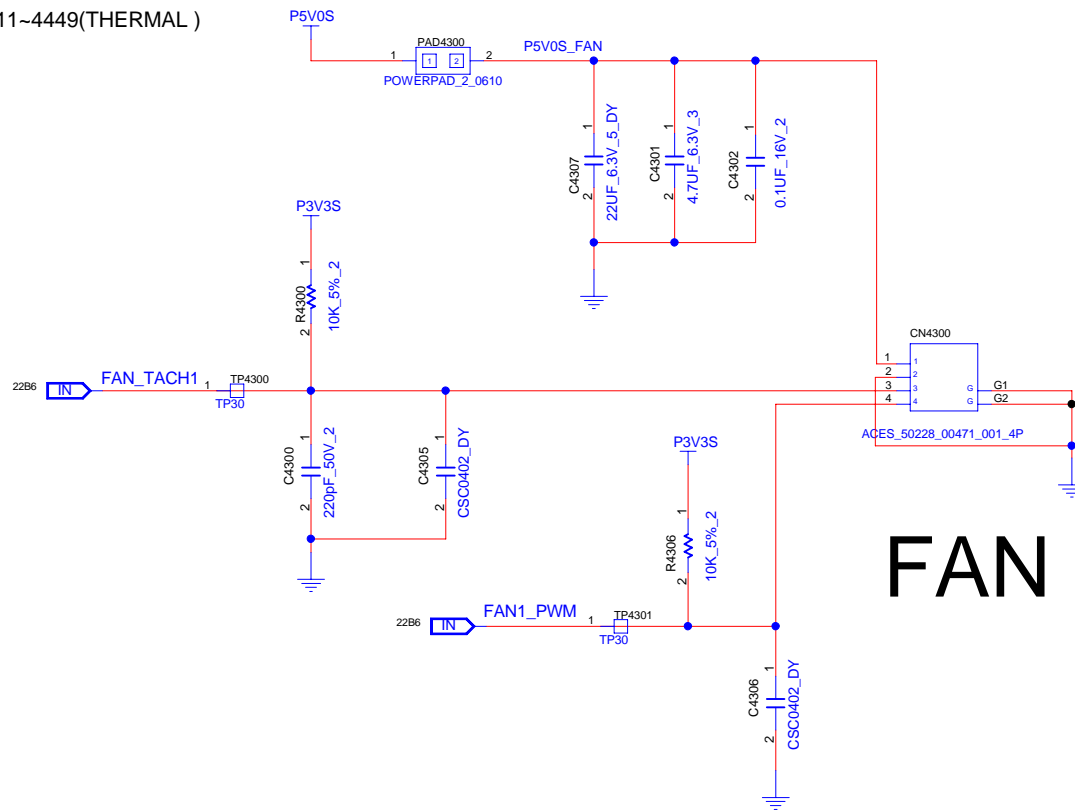


NOTE:  
SO-DIMMB SPD ADDRESS IS 0XA4  
SO-DIMMB TS ADDRESS IS 0X34

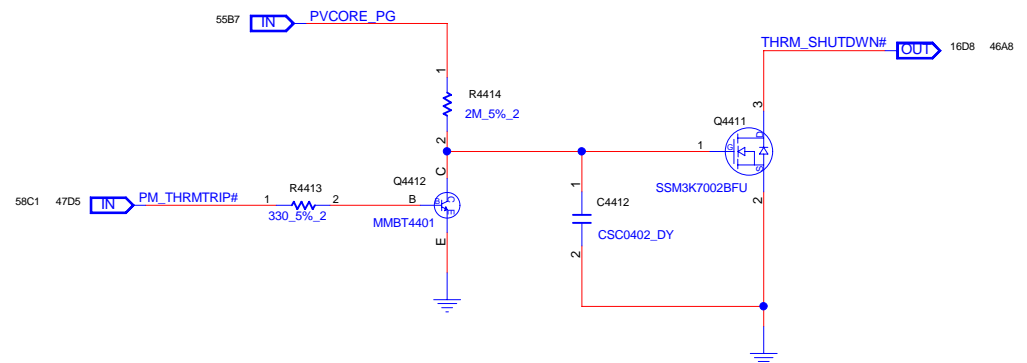
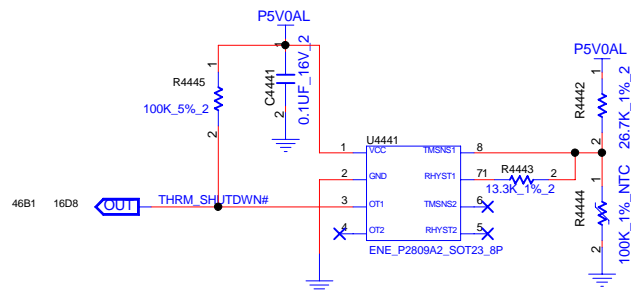
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxx-0-0			
SIZE	CODE	REV	
A3	CS	X01	

REFERENCE 4300~4349(FAN)  
REFERENCE 4411~4449(THERMAL )



# FAN CN



# INVENTEC

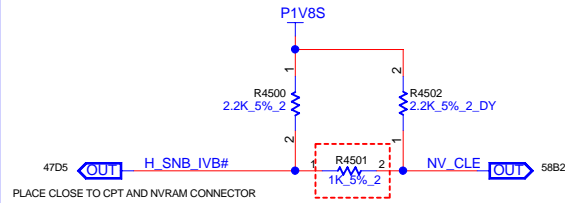
TITLE	MODEL, PROJECT, FUNCTION
	Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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CHANGE by	XXX	DATE	21-OCT-2002
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# REFERENCE 4500~4699(CPU)

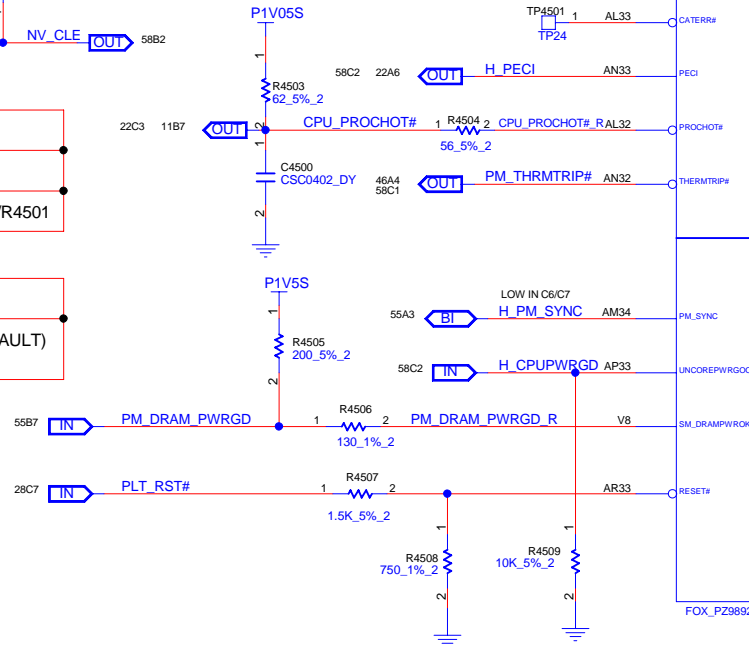


## PROCESS STRAP SETTING

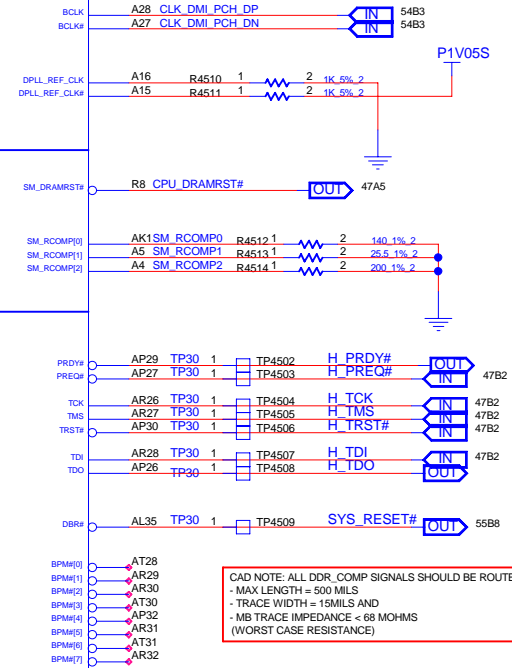
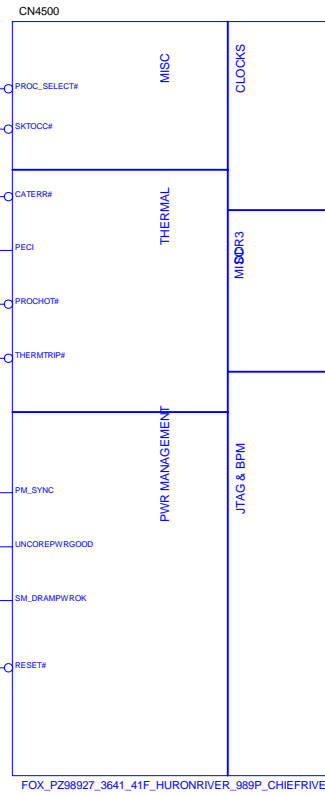
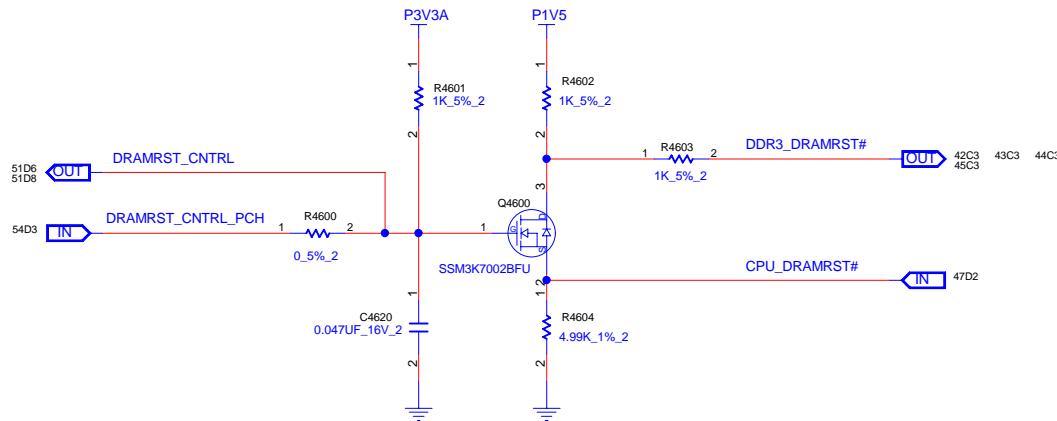
SANDY BRIDGE ONLY	STUFF R4502
SANDY BRIDGE/IVY BRIDGE	STUFF R4500/R4501

## DMI&FDI TERMINATIONVOLTAGE

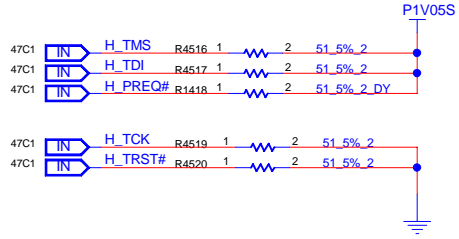
NV_CLE	SET TOVSS WHEN LOW (DEFAULT) SET TOVCC WHEN HIGH
--------	---



## S3 CIRCUIT: DRAM\_RST# TO MEMORY SHOULD BE HIGH DURING S3



CAD NOTE: ALL DDR\_COMP SIGNALS SHOULD BE ROUTED SUCH THAT  
- MAX LENGTH = 500 MILS  
- TRACE WIDTH = 15MILS AND  
- MB TRACE IMPEDANCE = 68 MOHMS  
(WORST CASE RESISTANCE)



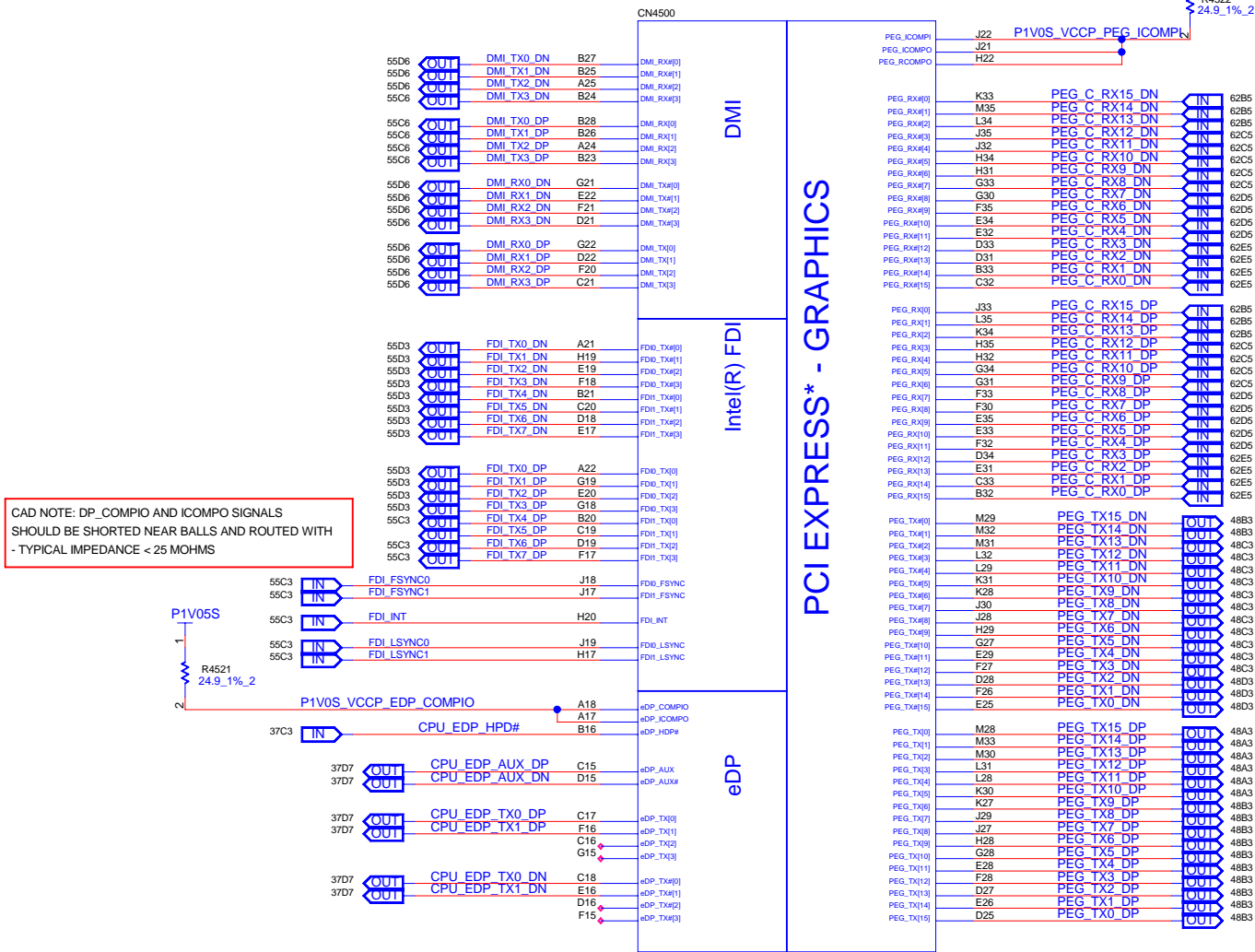
INVENTEC

TITLE		
MODEL,PROJECT,FUNCTION		
Block Diagram		
SIZE	CODE	DOC NUMBER
A3	CS	1310xxxxx-0-0
SHEET NO. OF		

CHANGE by XXX DATE 21-OCT-2002

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# REFERENCE 4500~4699(CPU)



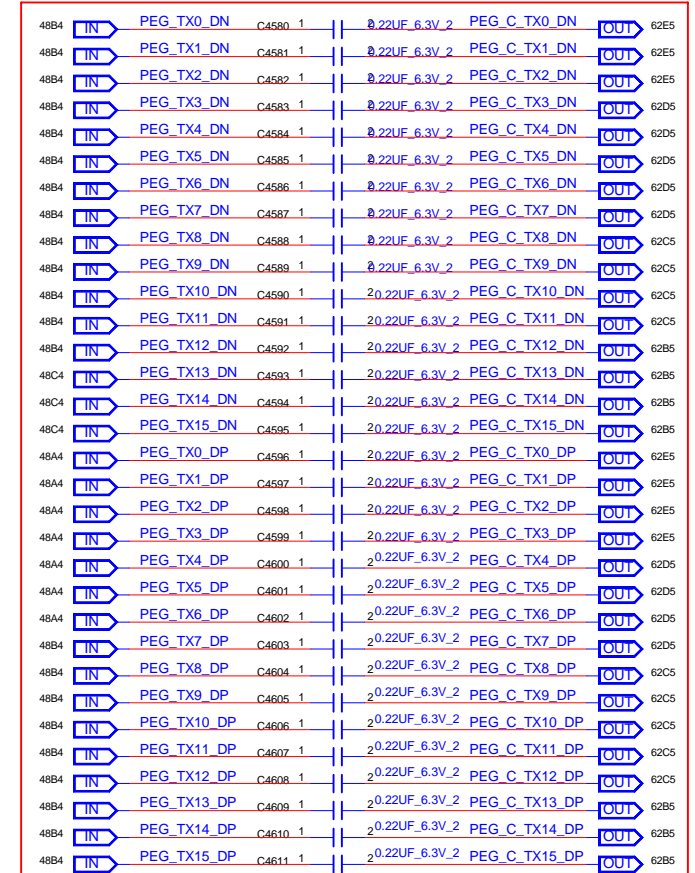
CAD NOTE: PEG\_ICOMPI AND RCOMPO SIGNALS SHOULD BE SHORTED AND ROUTED WITH

- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 43 MOHMS

PEG\_ICOMPO SIGNALS SHOULD BE ROUTED WITH

- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 14.5 MOHMS

## CLOSE TO CPU



FOX\_PZ98927\_3641\_41F\_HURONRIVER\_989P\_CHIEFRIVER

INVENTEC

TITLE  
MODEL PROJECT FUNCTION  
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

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CN4500



C

B

A

D

C

B

A

FOX\_PZ98927\_3641\_41F\_HURONRIVER\_989P\_CHIEFRIVER

FOX\_PZ98927\_3641\_41F\_HURONRIVER\_989P\_CHIEFRIVER

TITLE	MODEL PROJECT FUNCTION
1. <u>Project Description</u>	1. <u>Project Description</u>
2. <u>Project Objectives</u>	2. <u>Project Objectives</u>
3. <u>Project Scope</u>	3. <u>Project Scope</u>
4. <u>Project Organization</u>	4. <u>Project Organization</u>
5. <u>Project Schedule</u>	5. <u>Project Schedule</u>
6. <u>Project Budget</u>	6. <u>Project Budget</u>
7. <u>Project Risks</u>	7. <u>Project Risks</u>
8. <u>Project Communication</u>	8. <u>Project Communication</u>
9. <u>Project Monitoring and Control</u>	9. <u>Project Monitoring and Control</u>
10. <u>Project Closure</u>	10. <u>Project Closure</u>

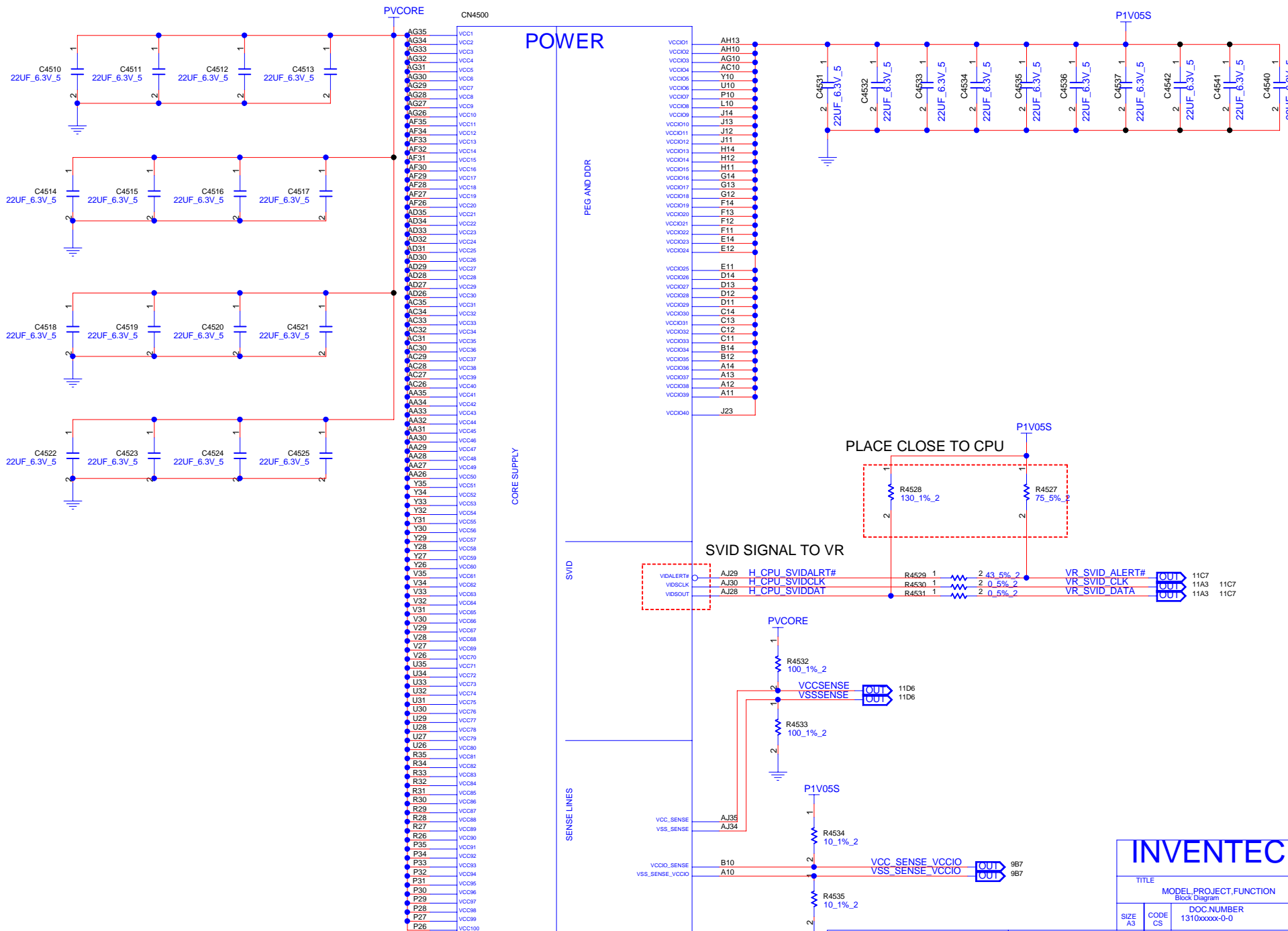
MODEL, PROJECT, FUNCTION		
Block Diagram		
		DOC NUMBER

SIZE A3	CODE CS	1310xxxx-0-0
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CHANGE by	XXX	DATE	21-OCT-2002
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# REFERENCE 4500~4699(CPU)



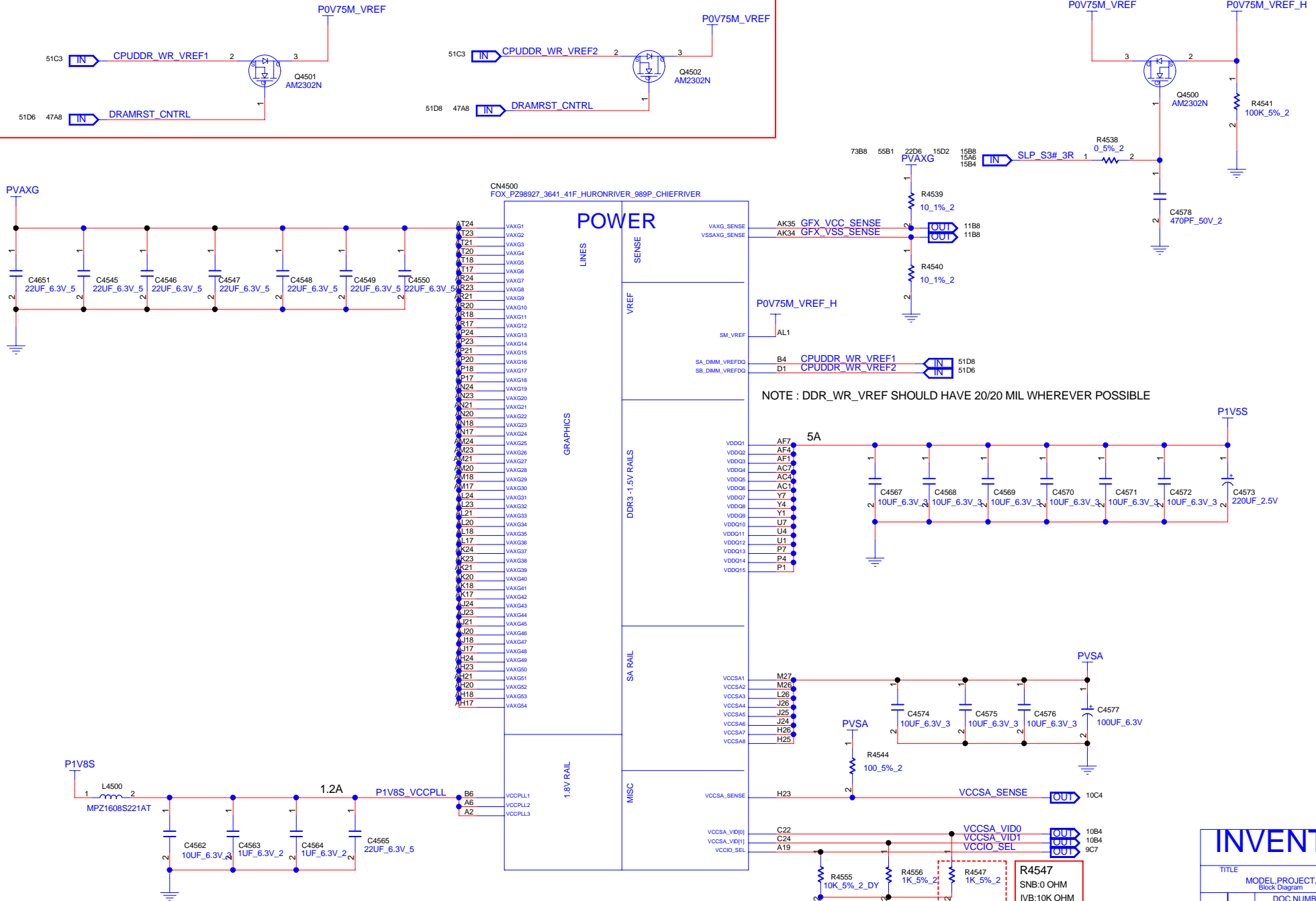
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

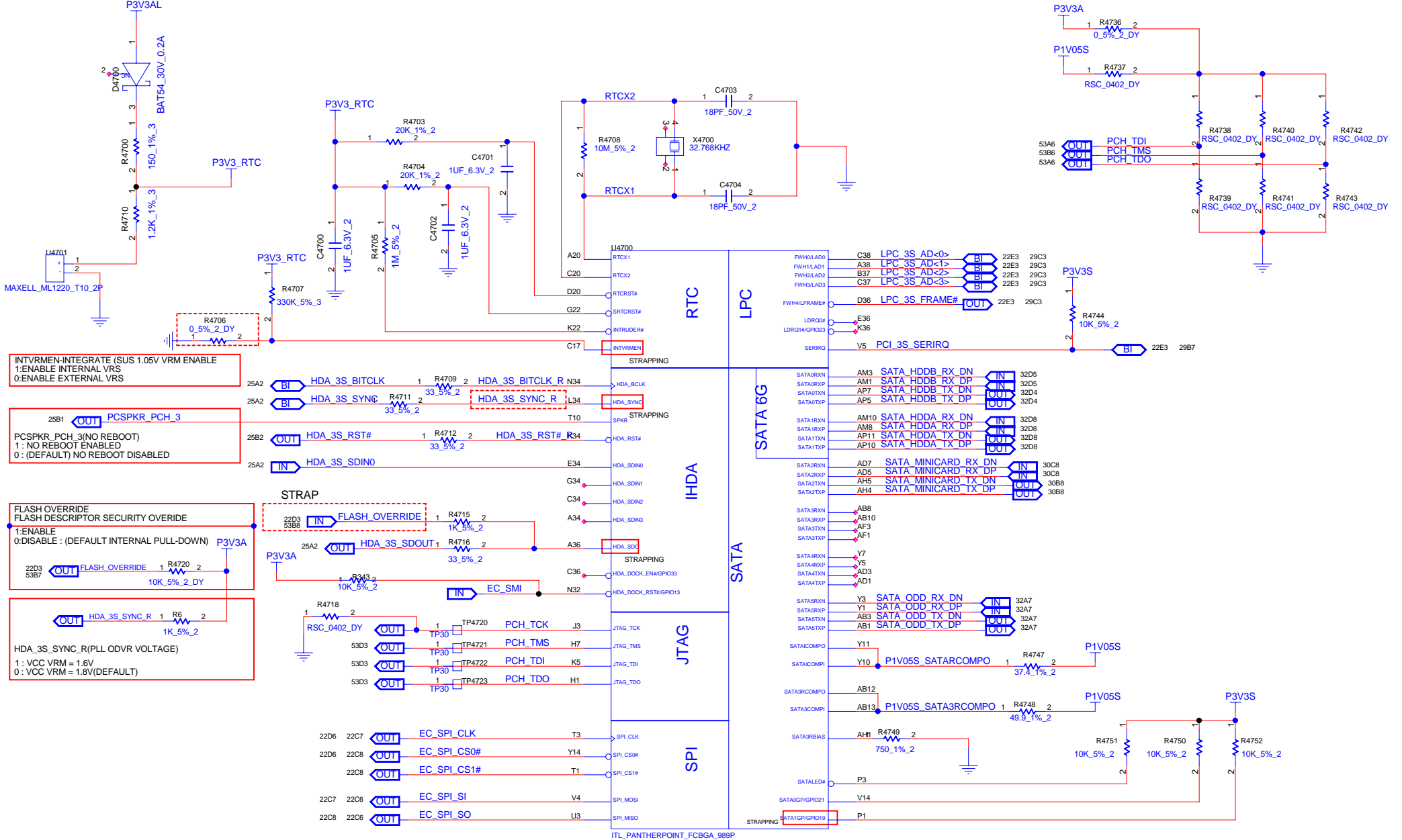
SHEET 50 of 76

PROCESSOR DRIVEN VREF PATH WAS STUFFED BY DEFAULT:  
ROUTE WITH MIN. TRACE WIDTH OF 10 MILS

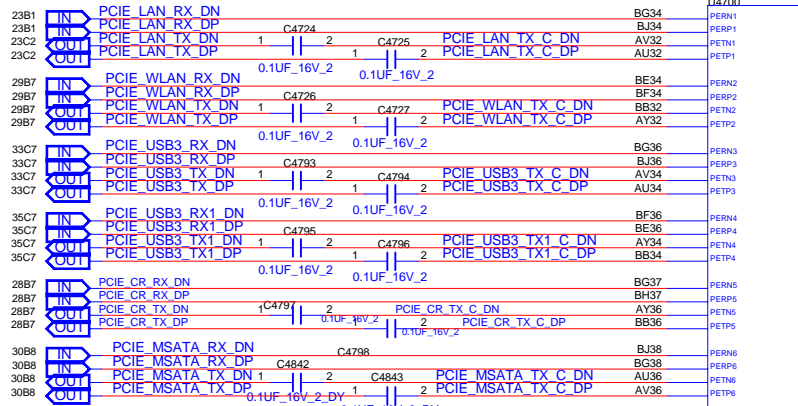




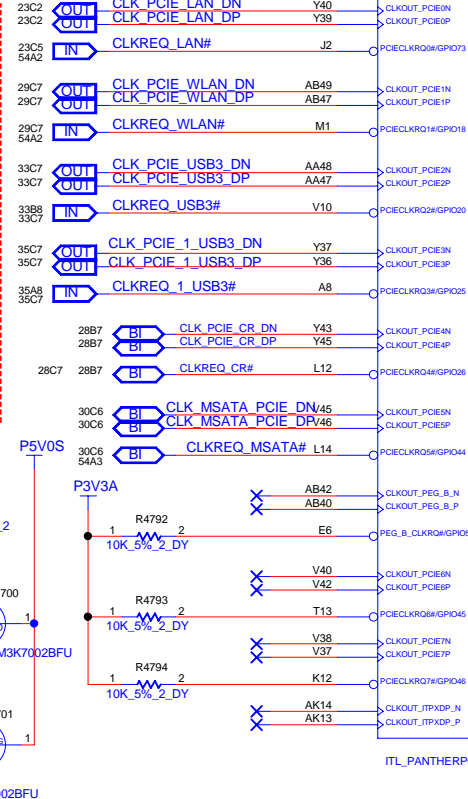
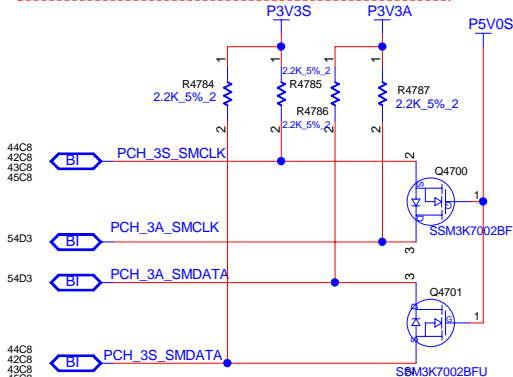
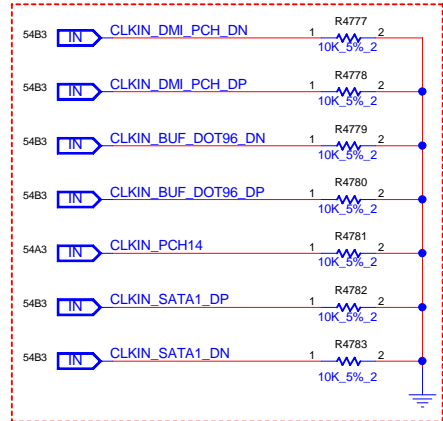
# REFERENCE 4700~4949(PCH)



# REFERENCE 4700~4949(PCH)



## CLOCK TERMINATION FOR FICM STUFF FOR INTEGRATED CLK



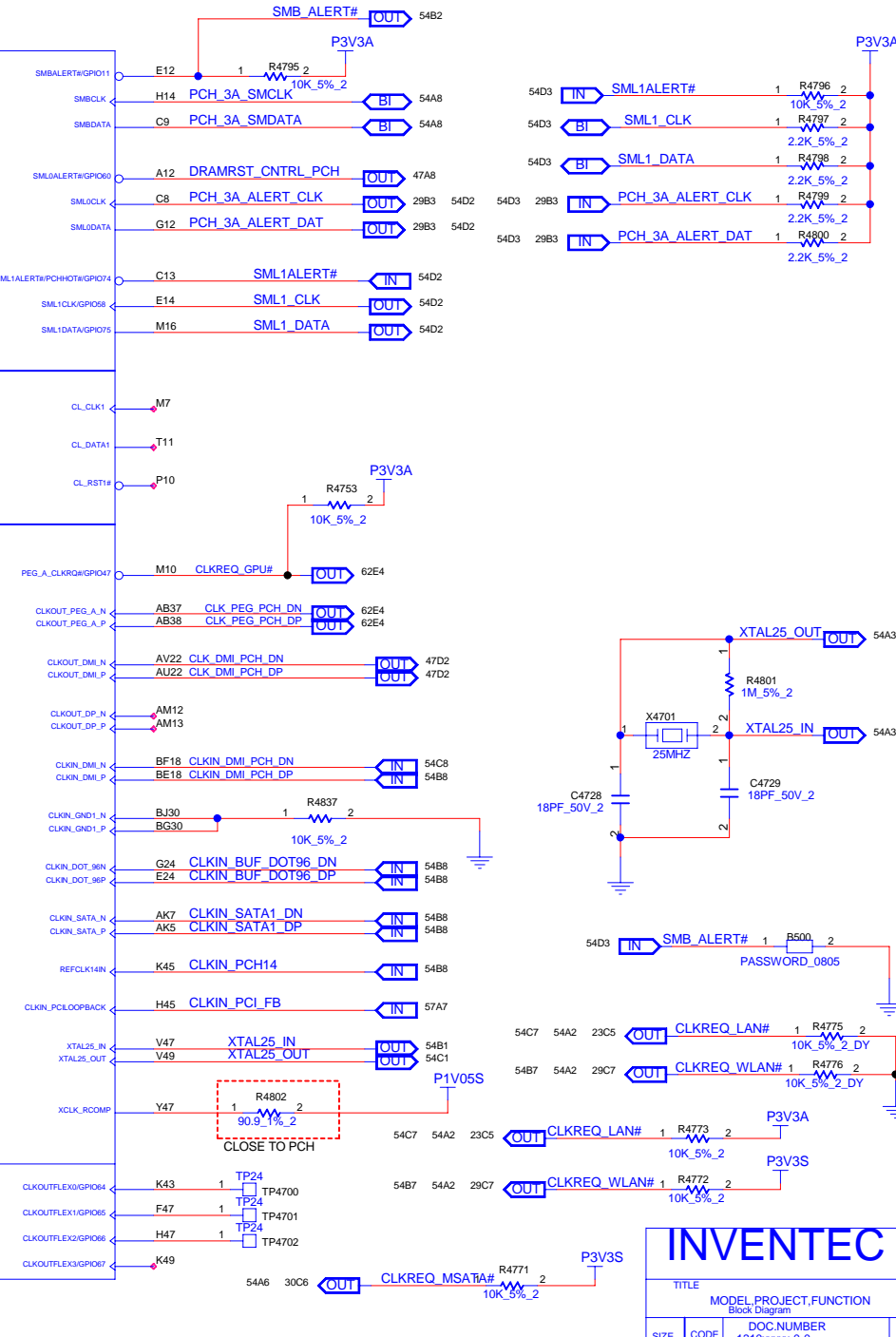
PCIE\*

CLOCKS

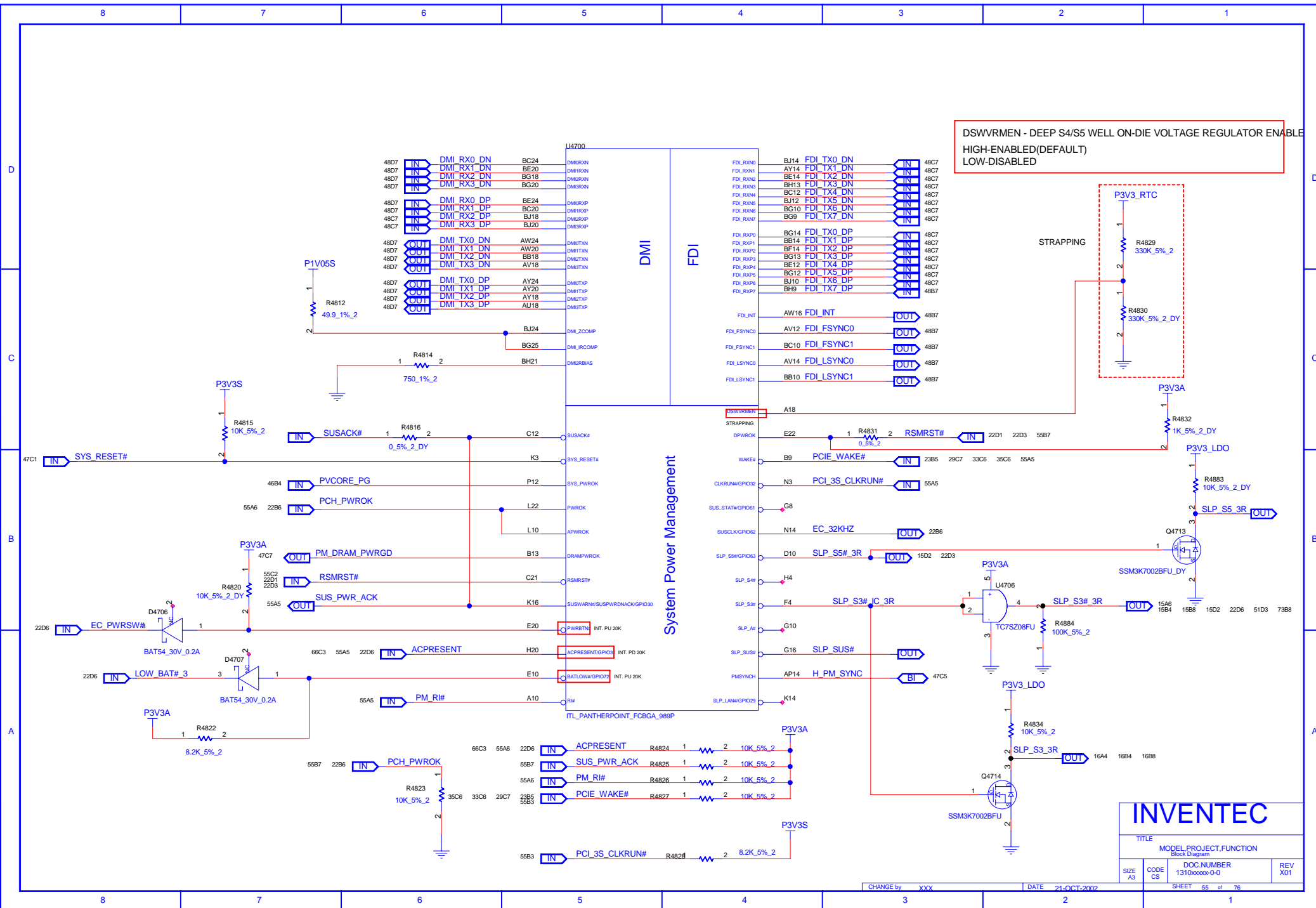
FLEX CLOCKS

SMBUS

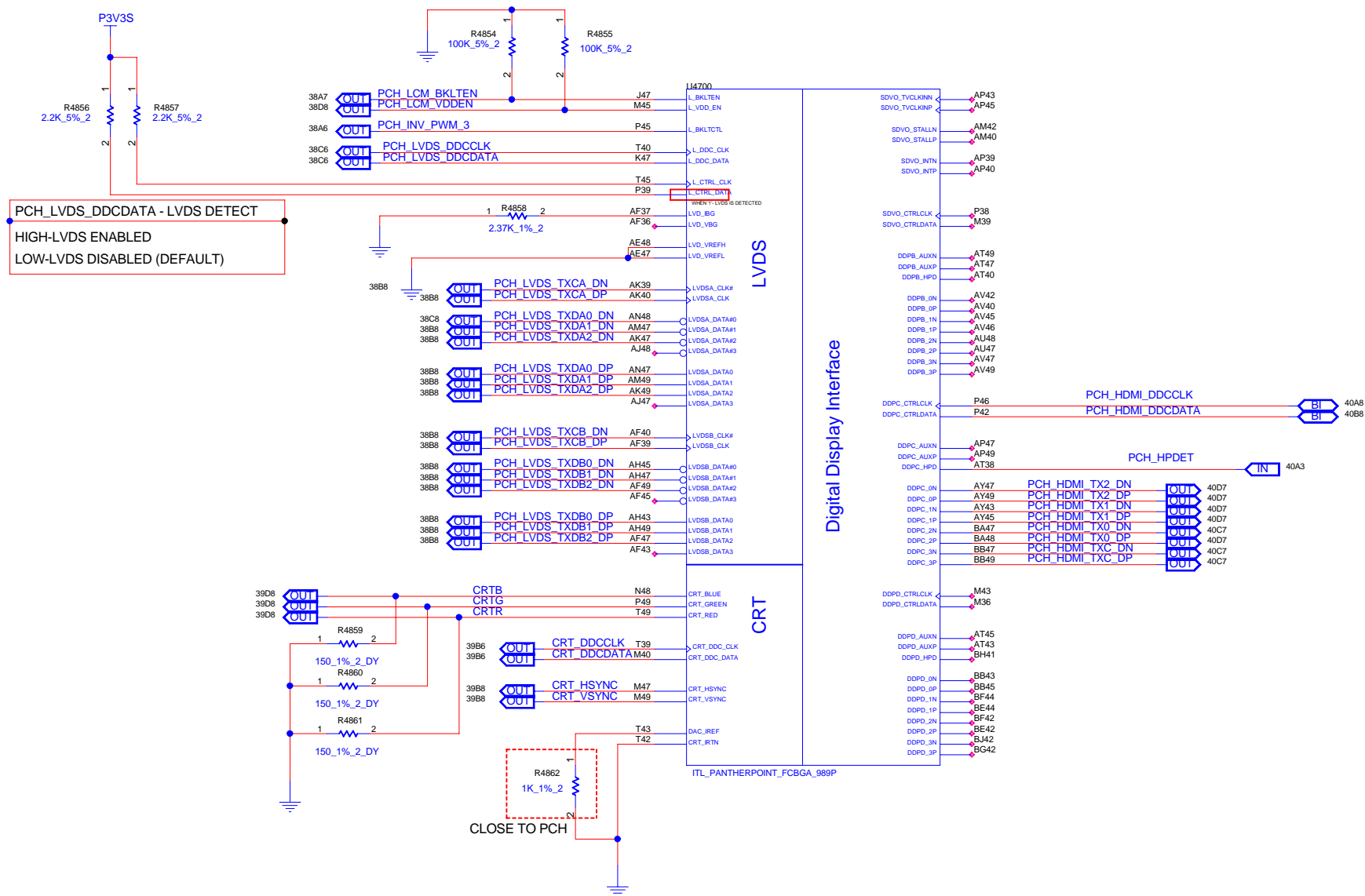
Controller Link



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
1310xxxxx-0-0			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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INVENTEC

TITLE

MODEL PROJECT FUNCTION

DOC NUMBER

1310xxxxx-0-0

REV

X01

CHANGE by XXX

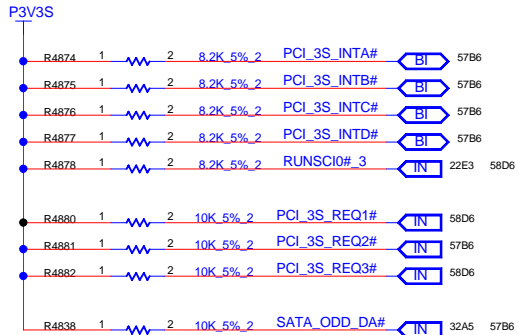
DATE 21-OCT-2002

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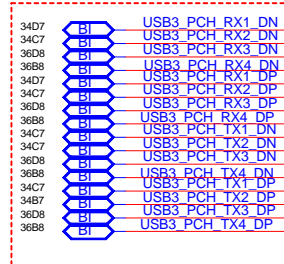


REFERENCE 4700-4949(PCH)

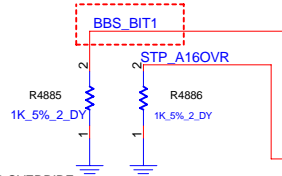
GPIO51 BBS_BIT1	GPIO19 BBS_BIT0	BOOT BIOS DESTINATION
0	1	RESERVED(NAND)
1	0	-----
1	1	SPI (DEFAULT)
0	0	LPC



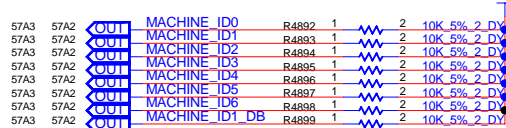
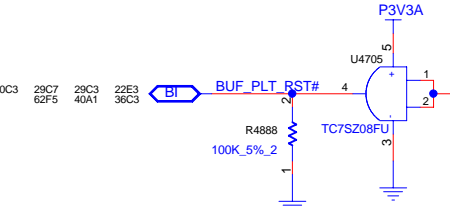
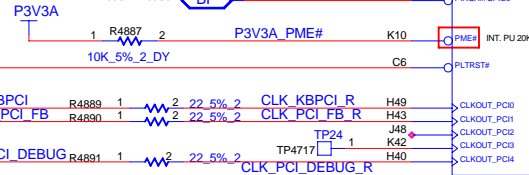
ROUTE WITH 90 OHMS IMPEDANCE  
TOTAL LENGTH NO LONGER THAN 11 INCHES



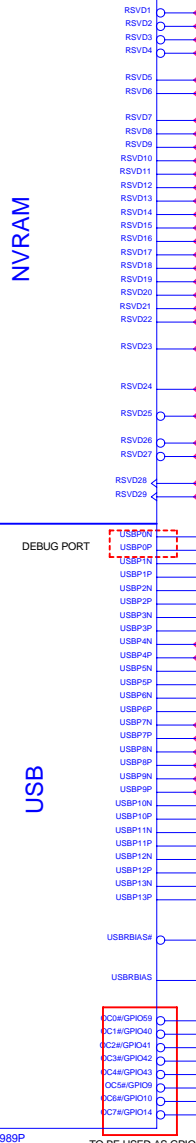
### BBS STRAPING



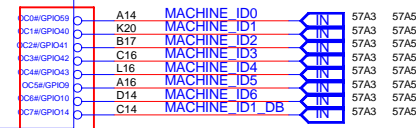
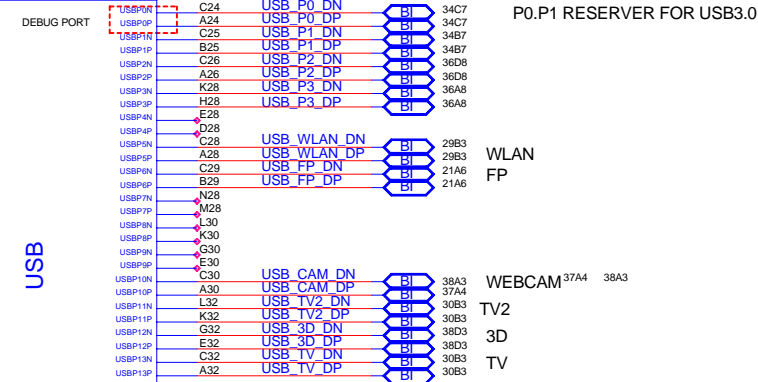
LOW=A16 SWAP OVERRIDE  
STP\_A160VR  
TOP-BLOCK SWAP OVERRIDE  
HIGH=DEFAULT



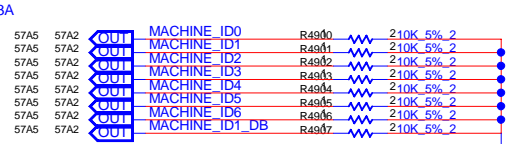
NOTE:10K\_5%(60130B1030ZT)



NOTE: USB2.0/3.0 COMBO-USB2.0 PORT 0,1 MAPPED USB3.0 PORT 1,2



TO BE USED AS GPIO



NOTE:10K\_5%(60130B1030ZT)

INVENTEC

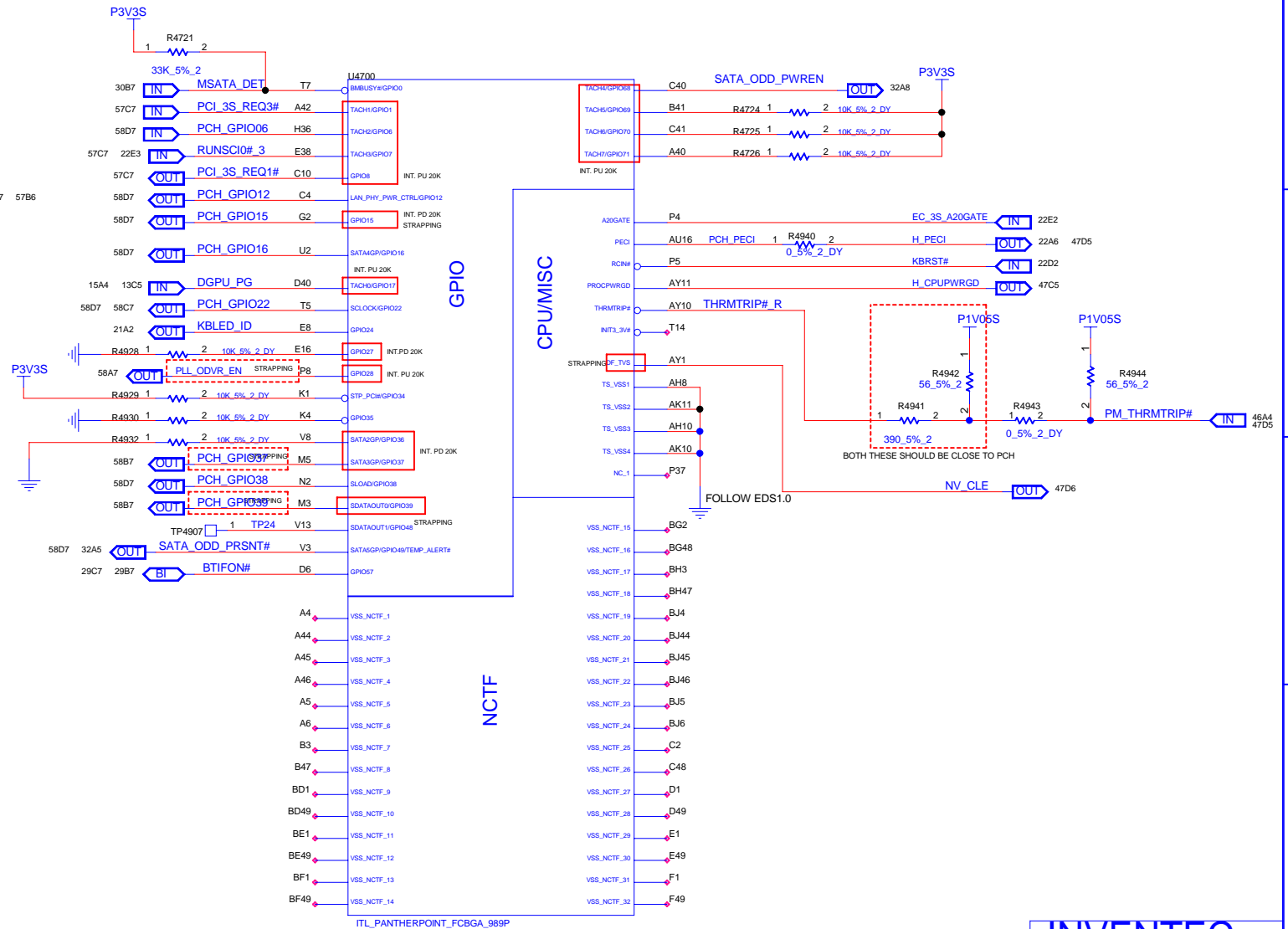
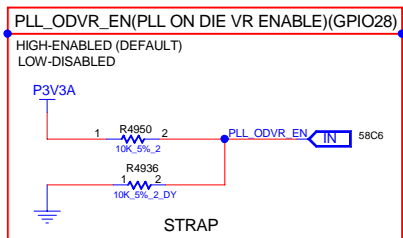
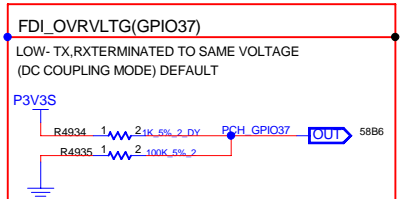
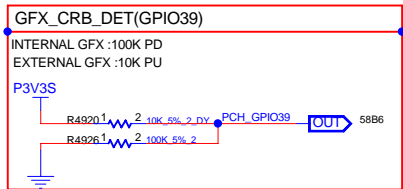
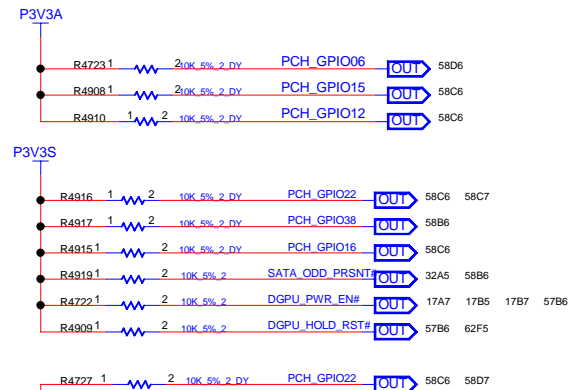
TITLE MODEL PROJECT.FUNCTION

Block Diagram

SIZE A3 CODE CS DOC.NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

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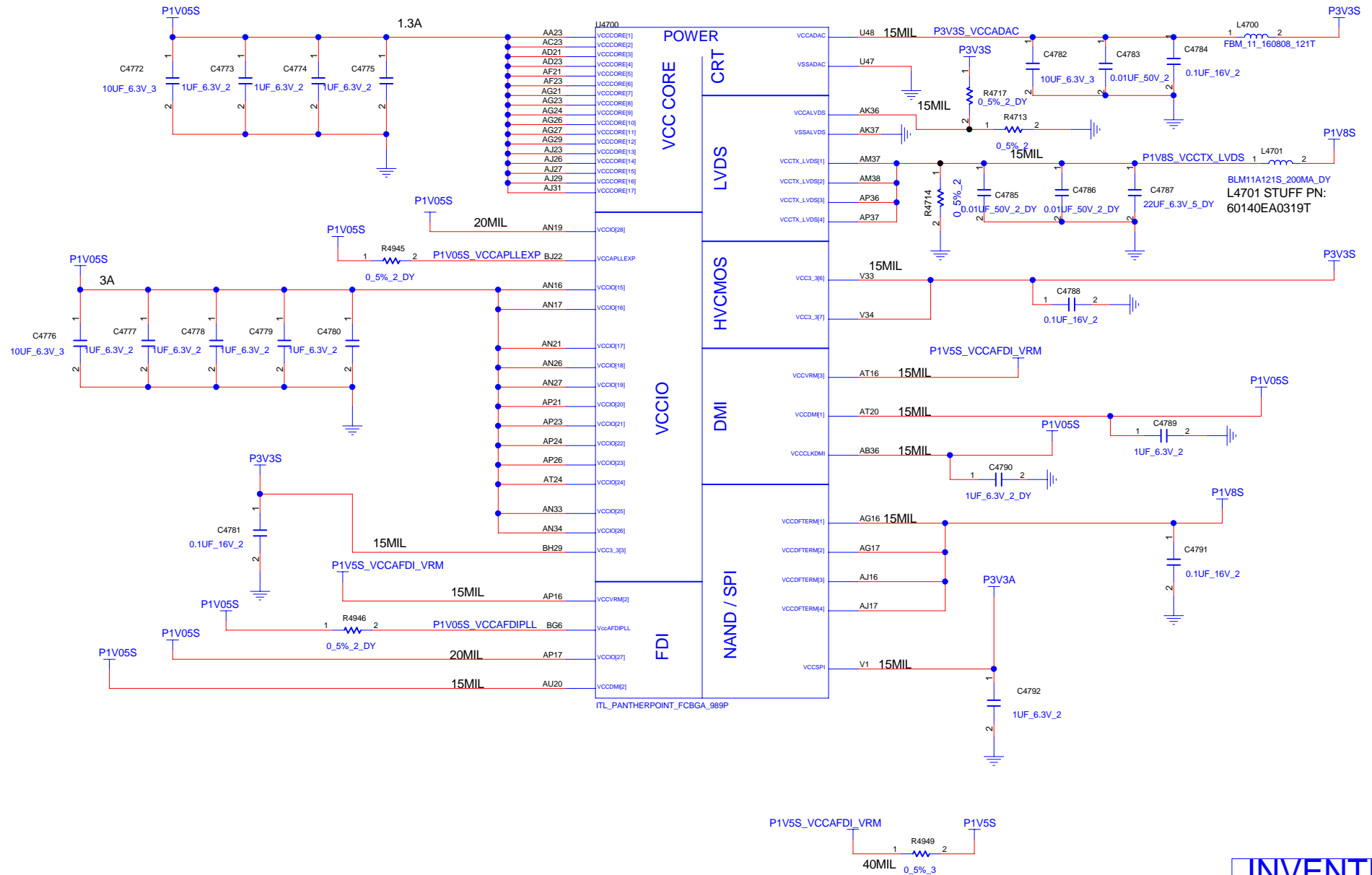
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01

CHANGE by XXX DATE 21-OCT-2002

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REFERENCE 4700~4949(PCH)



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			

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8

7

6

5

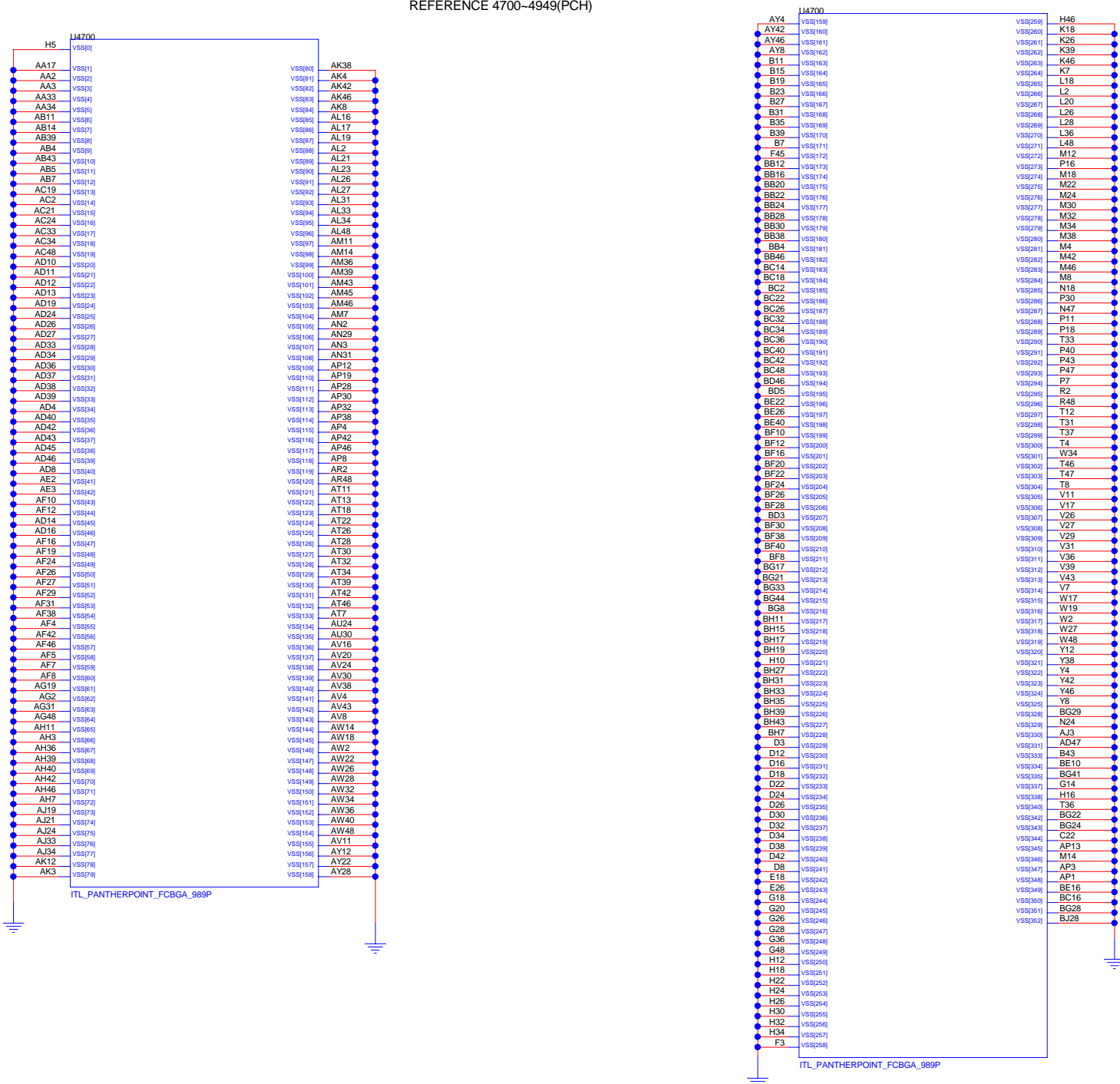
4

3

2

1

REFERENCE 4700~4949(PCH)



INVENTEC

TITLE  
MODEL, PROJECT, FUNCTION  
Block DiagramSIZE A3 CODE CS DOC. NUMBER  
1310xxxxx-0-0 REV X01

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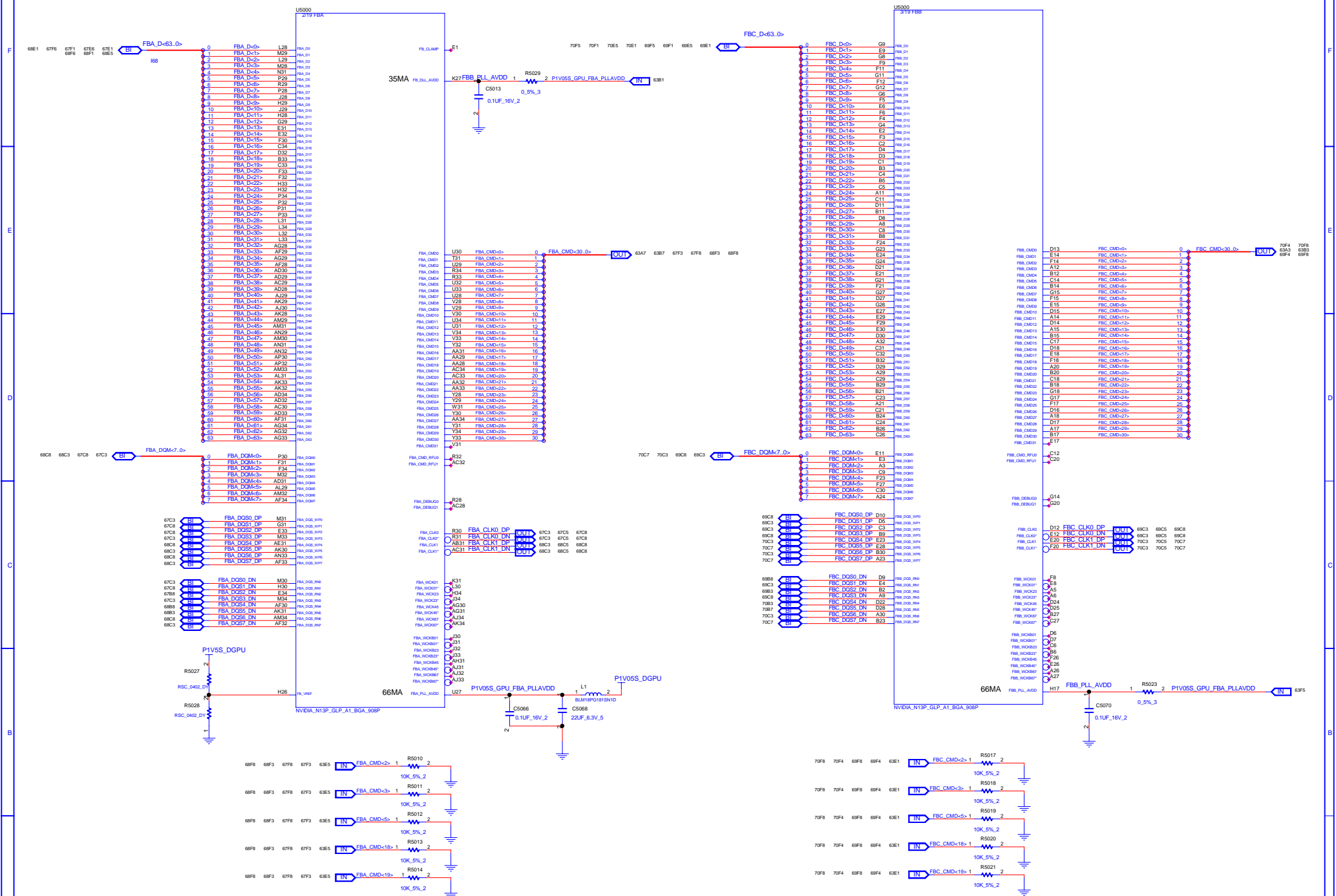
CHANGE by XXX

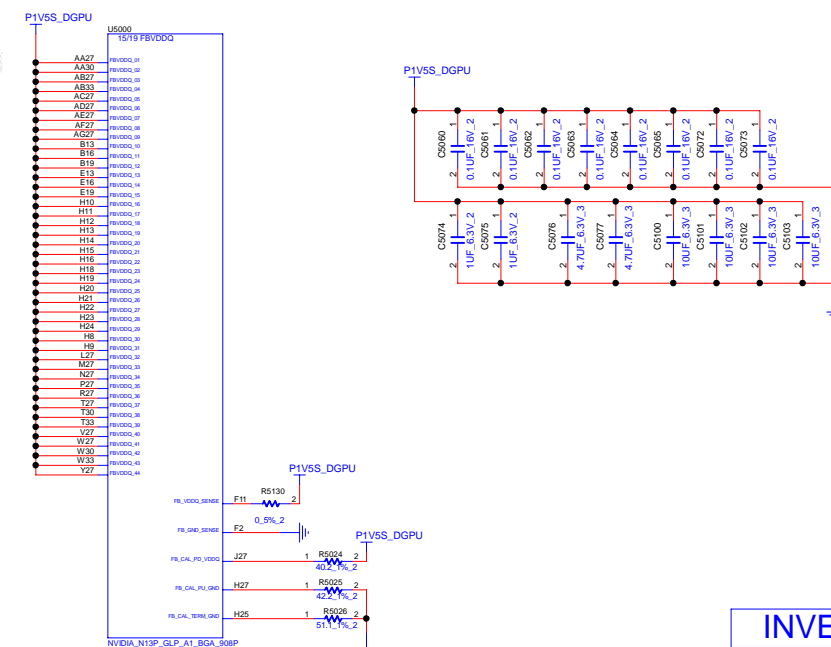
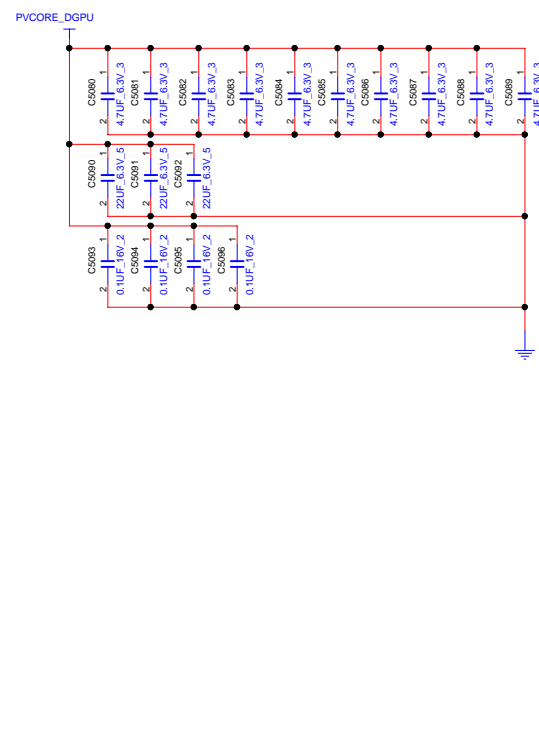
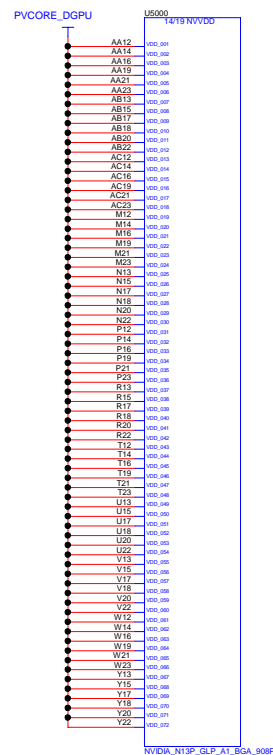
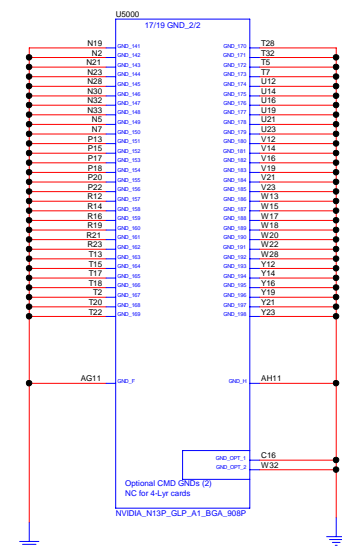
DATE 21-OCT-2002

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FB\_CLAMP  
N13P-GV, GLP-NC  
N13P-GV, N13M-GS, N13P-GT-GS-LP: PL10K





<h1 style="text-align: center;">INVENTEC</h1>			
TITLE <h2 style="text-align: center;">MODEL PROJECT FUNCTION</h2> <p style="text-align: center;">Block Diagram</p>			
SIZE C	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV XD1
SHEET 64 of 76			





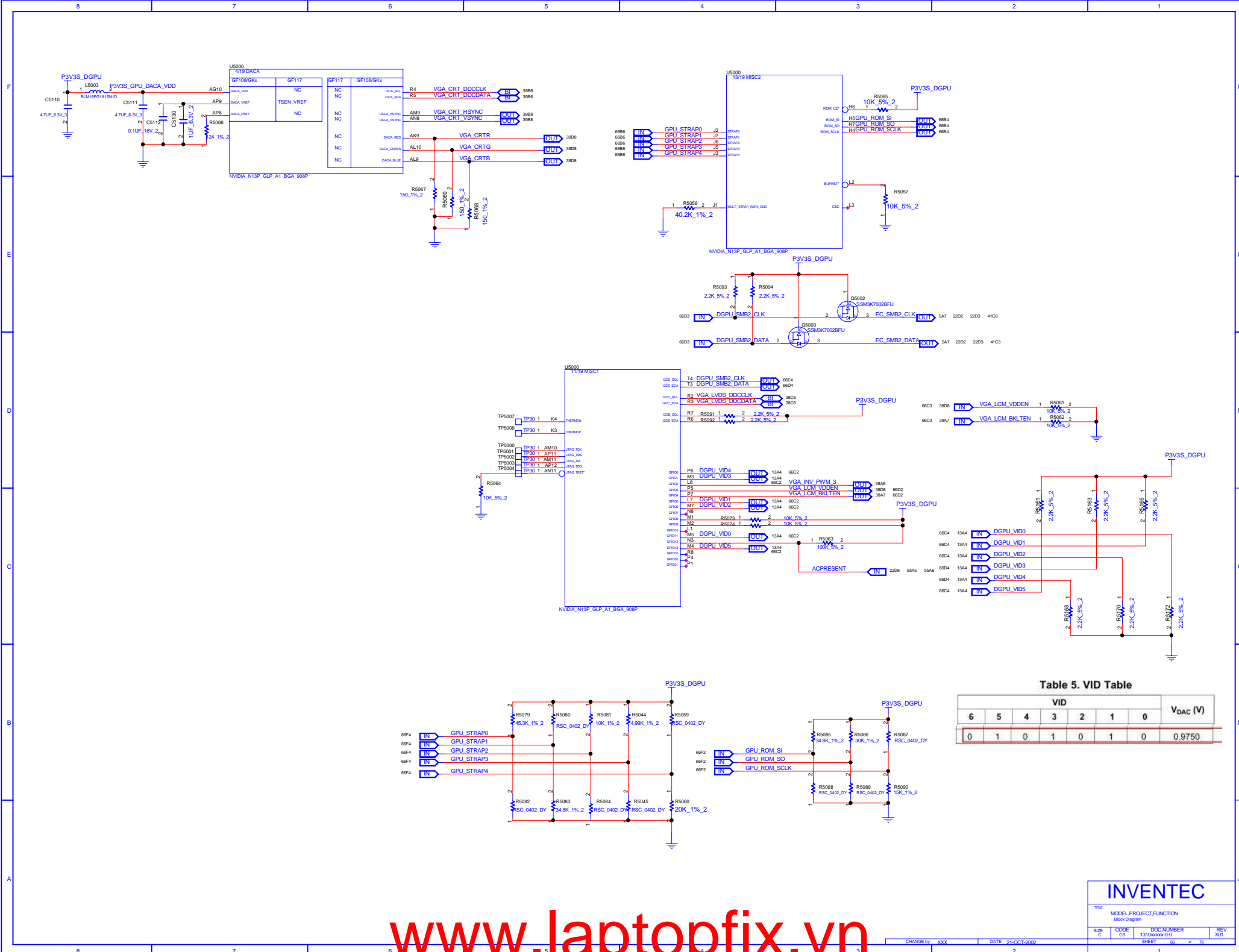


Table 5. VID Table							
VID							V <sub>DAC</sub> (V)
6	5	4	3	2	1	0	
0	1	0	1	0	1	0	0.9750

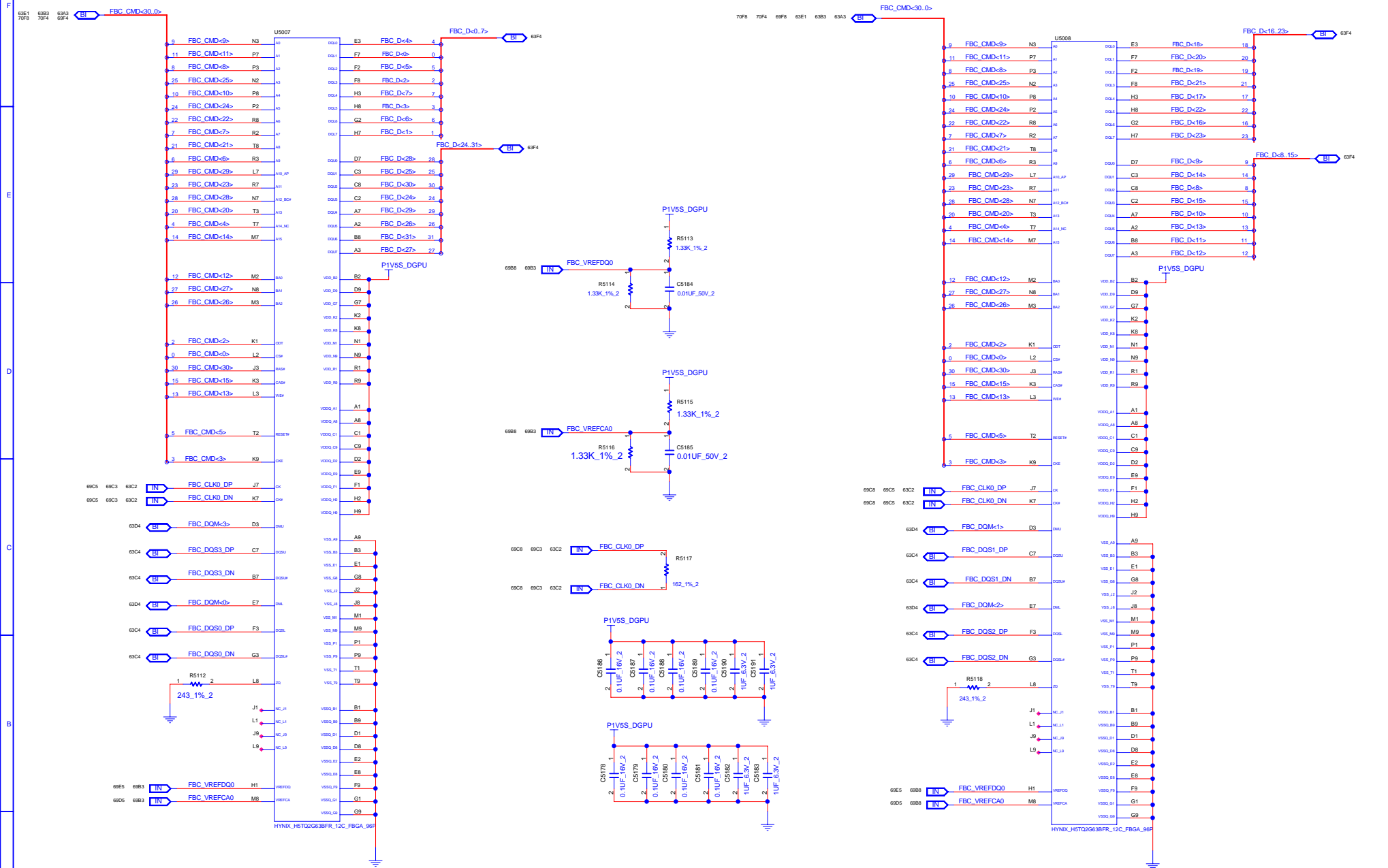




# INVENTEC

TITLE			
MODEL/PROJECT/FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	C3	131000000-03	X01
SHEET		99	of 99

CHANGE BY: XXXX DATE: 21-04-2006





D

## D



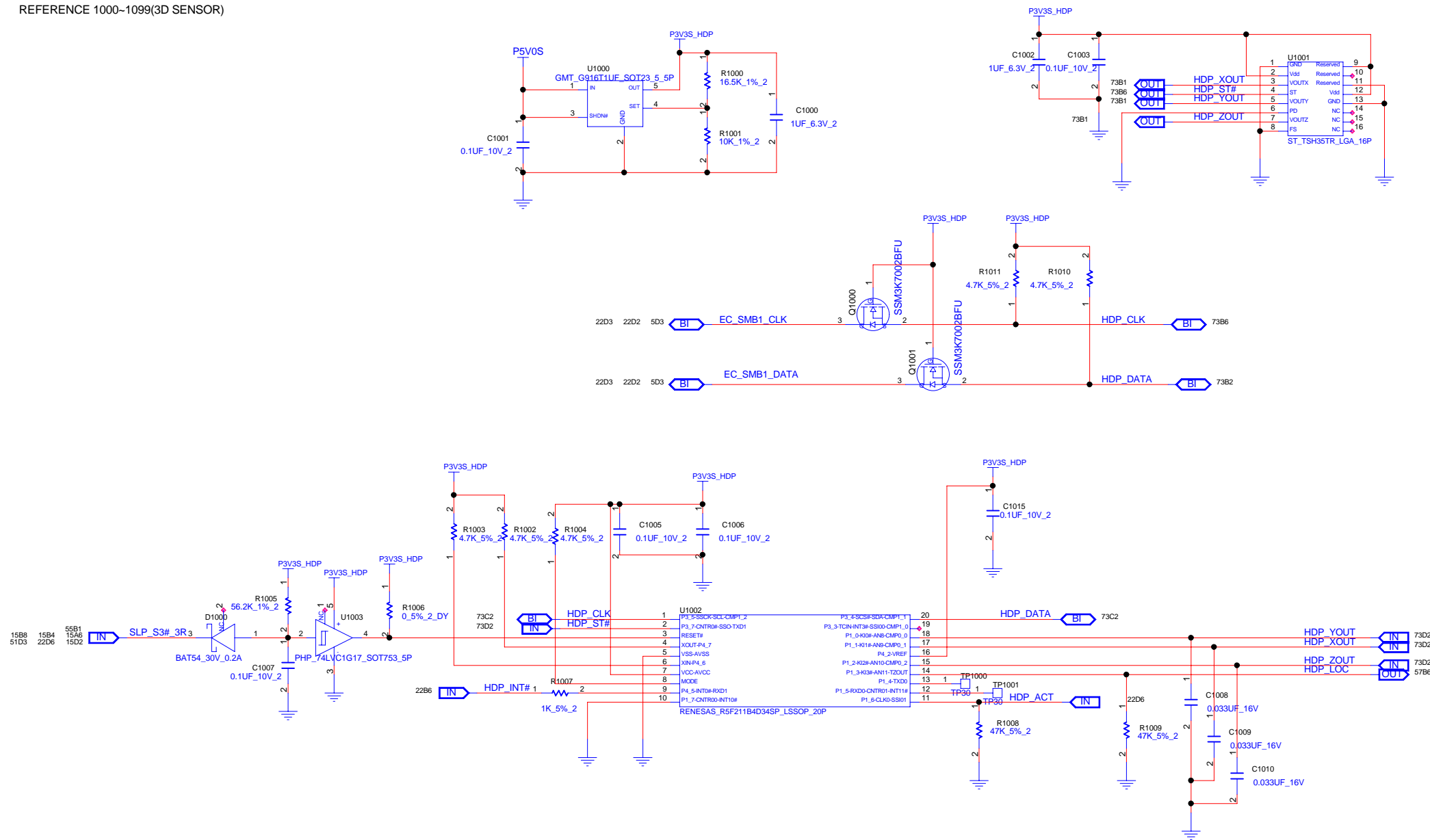
B

A





REFERENCE 1000~1099(3D SENSOR)



INVENTEC

TITLE  
MODEL PROJECT FUNCTION  
Block Diagram  
DOC NUMBER  
1310xxxxx-0-0  
REV  
X01

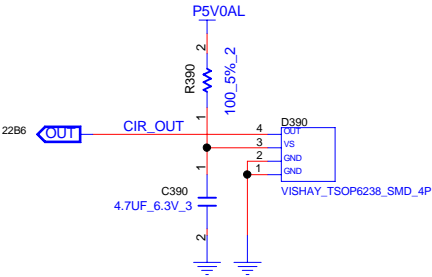
CHANGE by XXX DATE 21-OCT-2002

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REFERENCE 390~399

# CIR

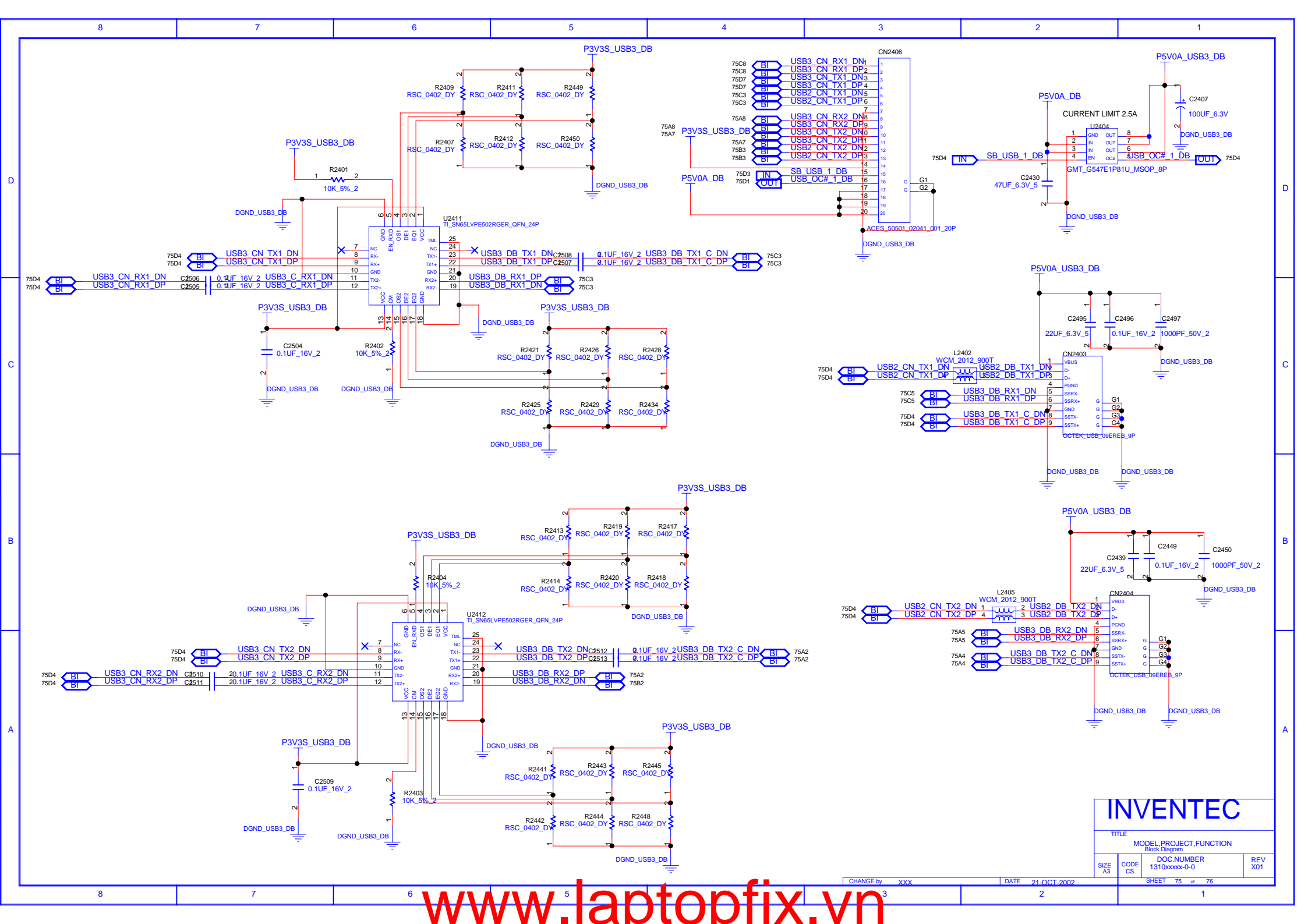


INVENTEC

TITLE MODEL PROJECT FUNCTION  
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

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INVENTEC

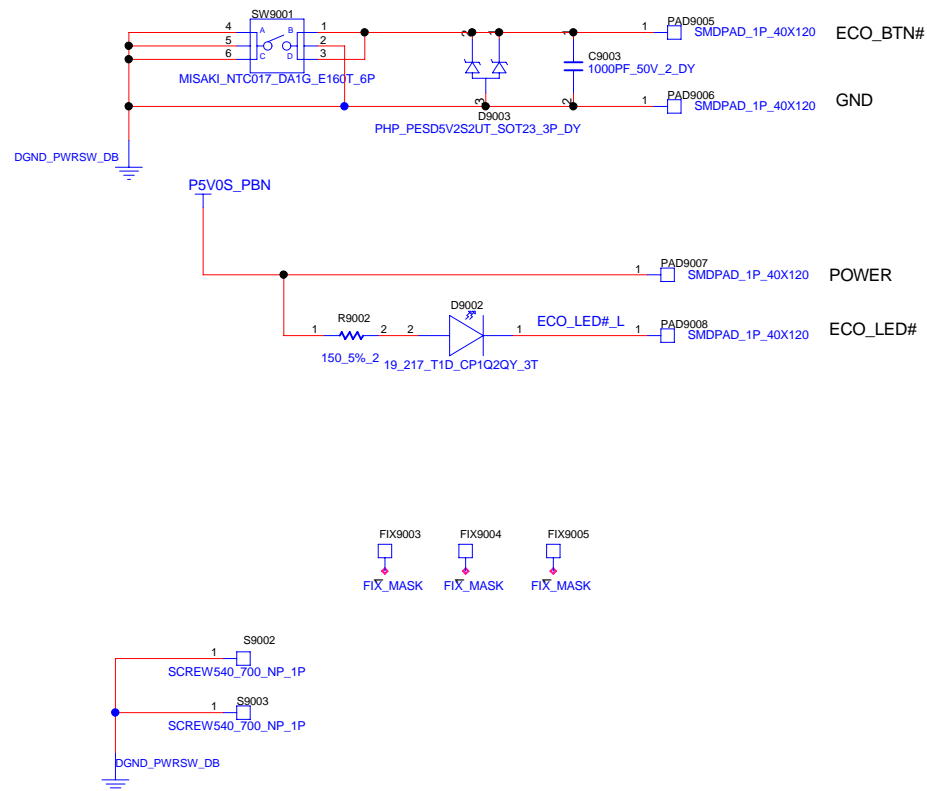
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE		DOC NUMBER	
A3	CS	1310xxxx-0-0	
REV		X01	

CHANGE by XXX DATE 21-OCT-2002

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# 3D/ECO VISION



INVENTEC

TITLE MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxxx-0-0

REV X01

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CHANGE by XXX

DATE 21-OCT-2002